

Compal Confidential

DCL51 Schematics Document

Banias uFCBGA/uFCPGA Package with Montara-GM Core Logic

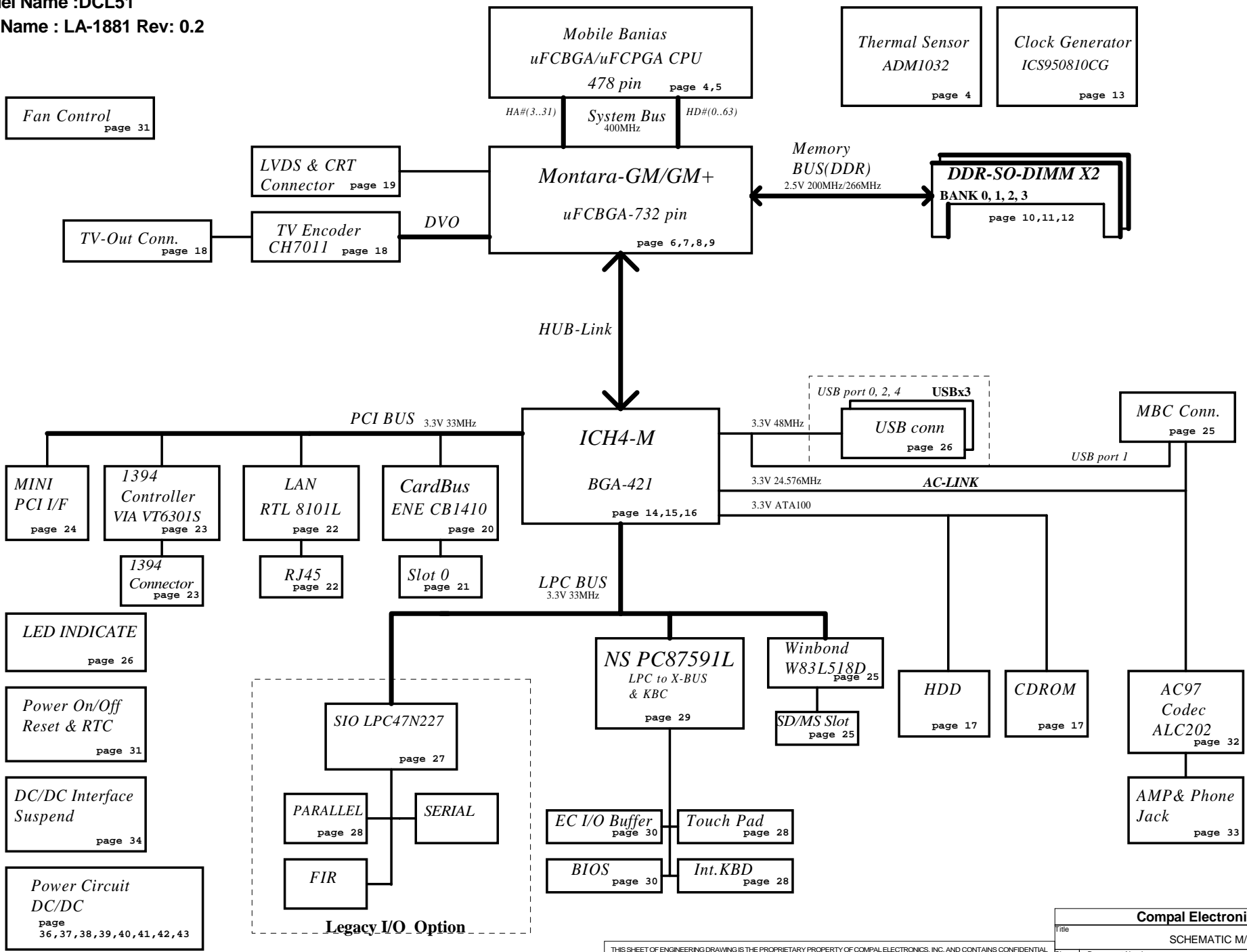
2003-06-12

REV: 1.0

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SCHEMATIC M/B LA-1881		
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Model Name :DCL51
File Name : LA-1881 Rev: 0.2



Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+VCCP	1.05V rail for Processor I/O	ON	OFF	OFF
+1.2VS	1.2VS switched power rail for GMCH	ON	OFF	OFF
+1.25VS	1.25V switched power rail	ON	OFF	OFF
+1.5VALW	1.5V power rail	ON	ON	ON
+1.5V	1.5V power rail	ON	ON	OFF
+1.5VS	1.5V power rail for GMCH	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5V	2.5V power rail	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5V	5V power rail	ON	ON	OFF
+5VS	5V switched power rail	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

Board ID Table

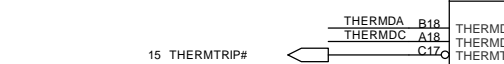
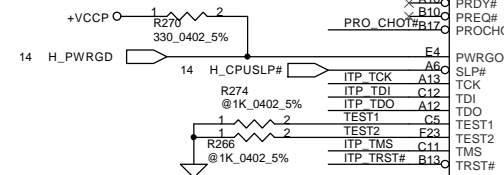
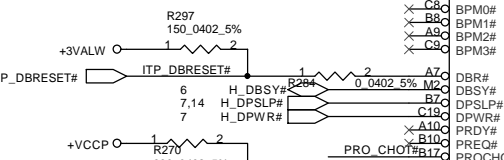
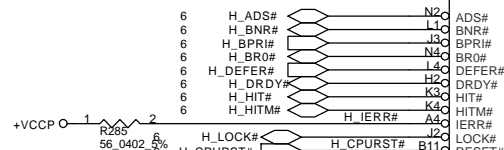
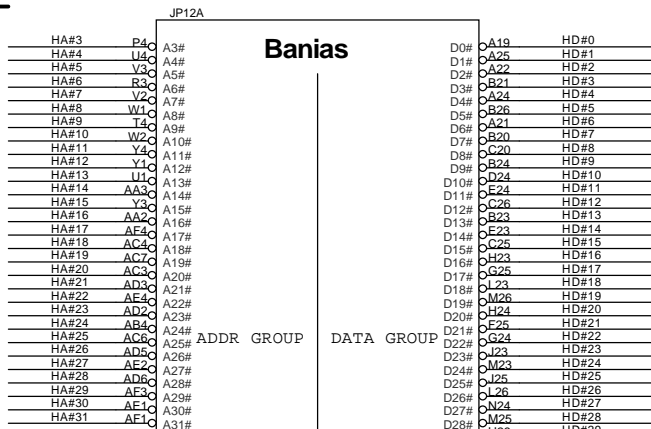
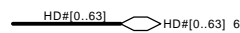
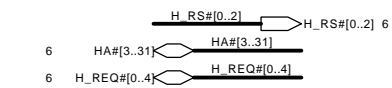
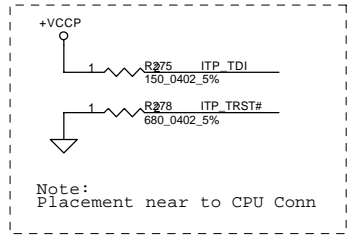
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0	0	1	0.2 (For 030)
0	1	0	0.3 (For 030)
0	1	1	1.0 (For 030)
1	0	0	0.1 (For ABO)
1	0	1	0.2 (For ABO)
1	1	0	0.3 (For ABO)
1	1	1	1.0 (For ABO)

External PCI Devices

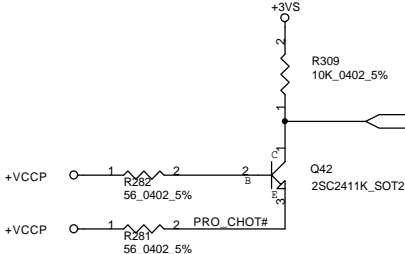
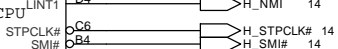
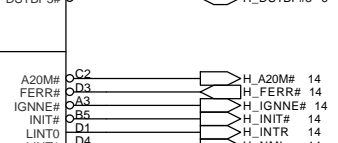
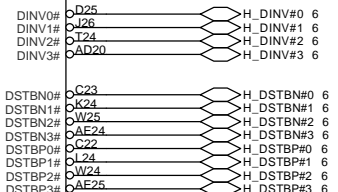
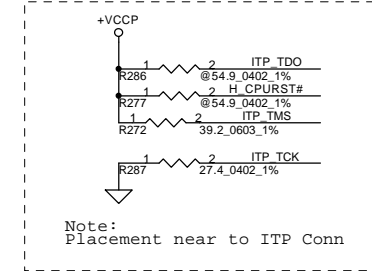
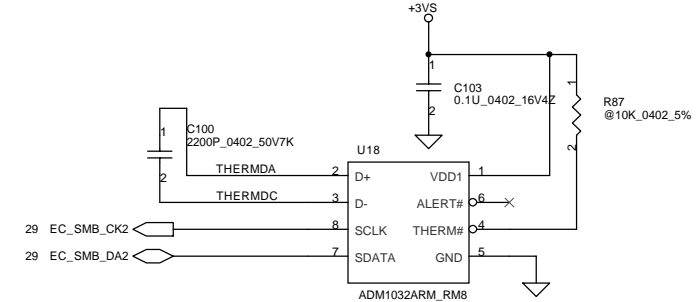
Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA
LAN	AD17	3	PIRQF
Mini-PCI	AD18/AD20	1/4	PIRQG/PIRQH
1394	AD16	0	PIRQE

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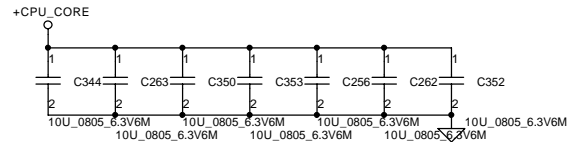
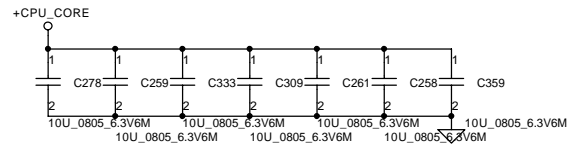
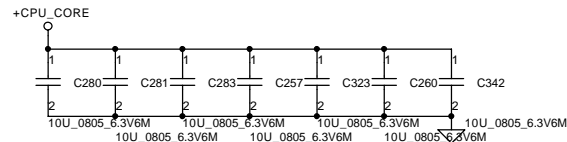
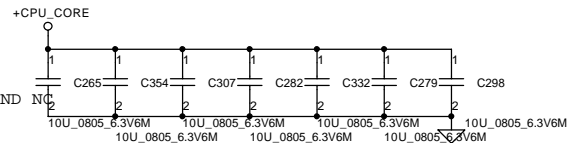
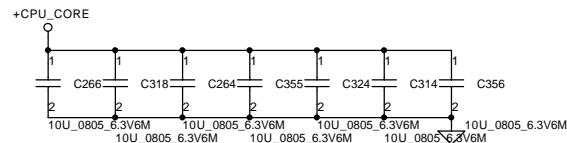
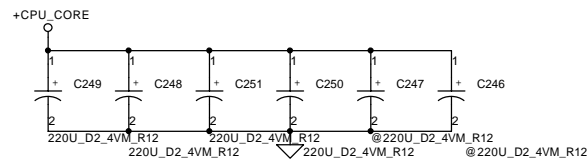
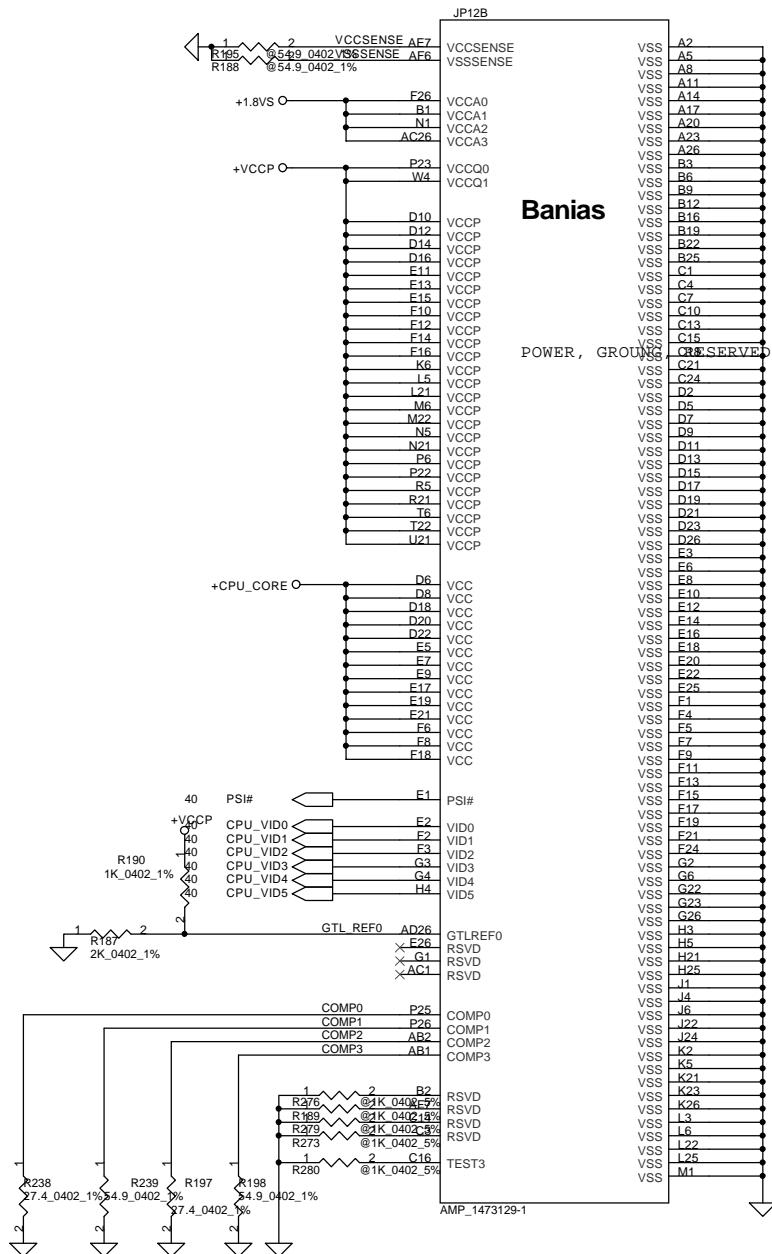


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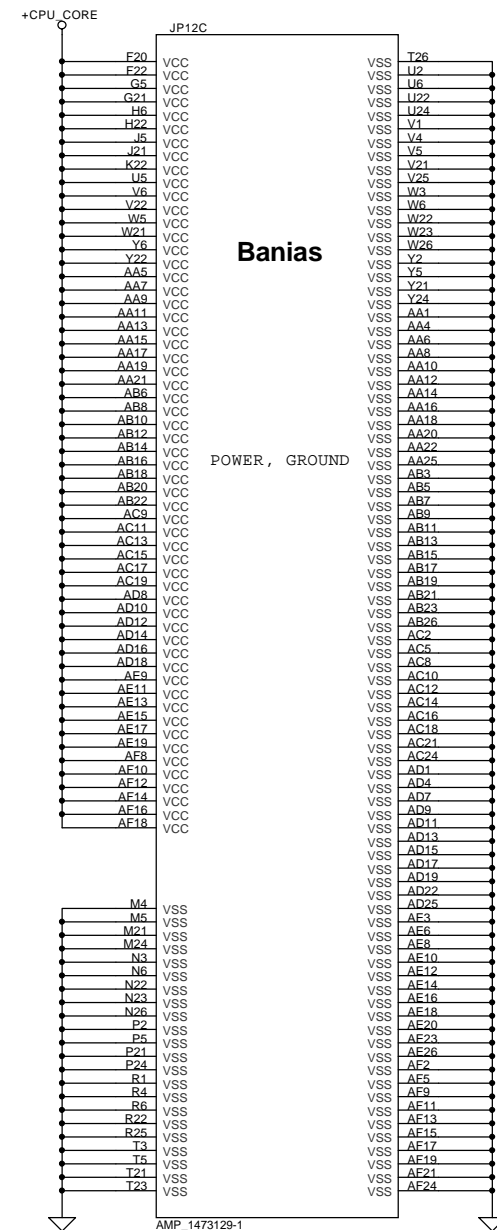
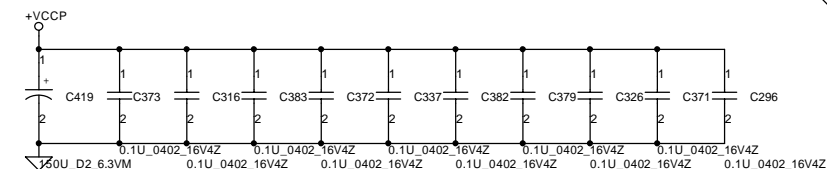
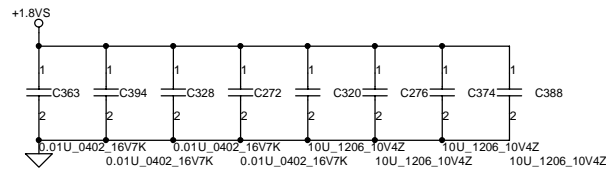


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Vcc-core Decoupling	C, uF	ESR, mohm	ESL, nH
SPCAP, Polymer	4X220uF	12m ohm/4	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35



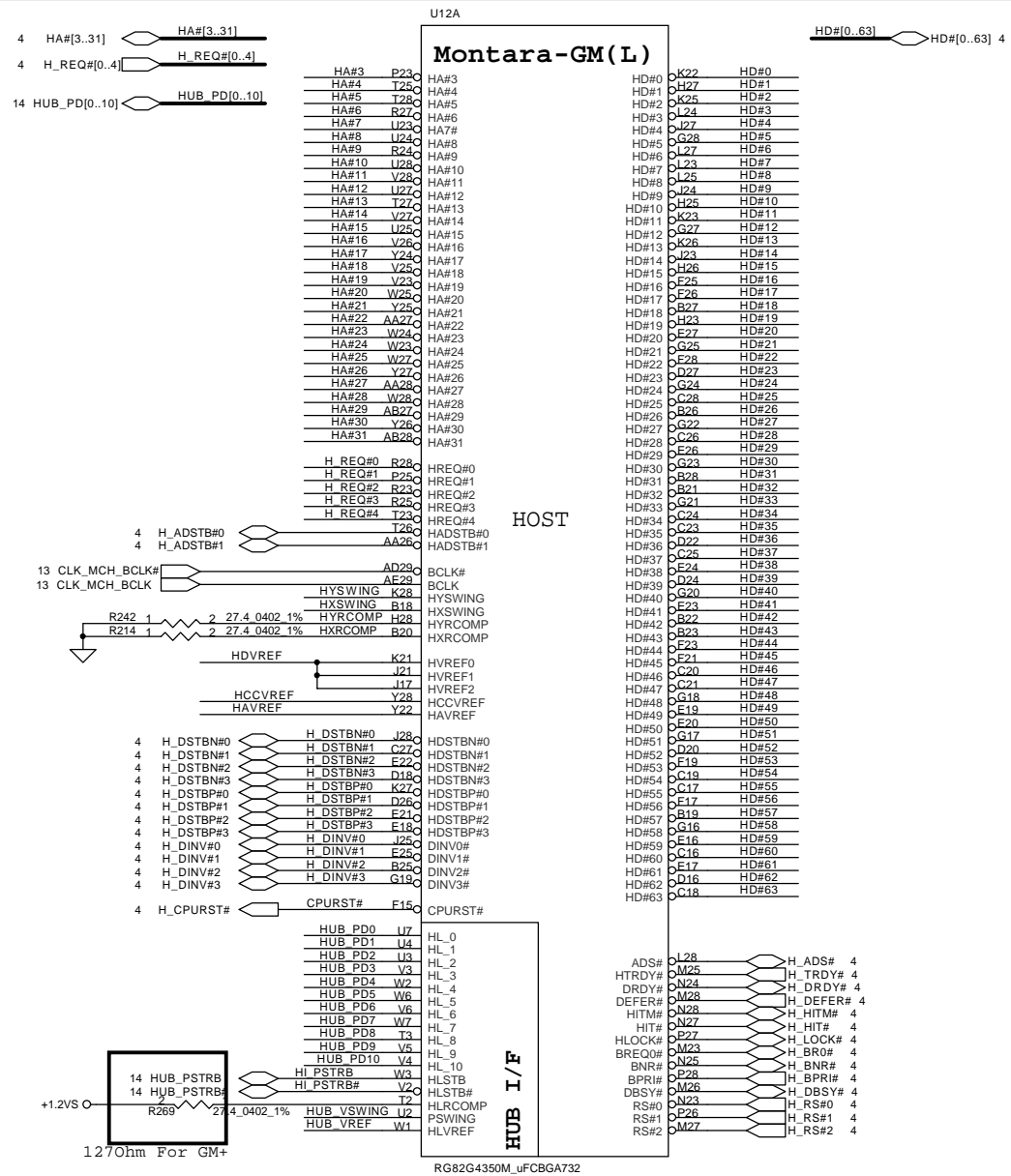
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Schematic M/B LA-1881

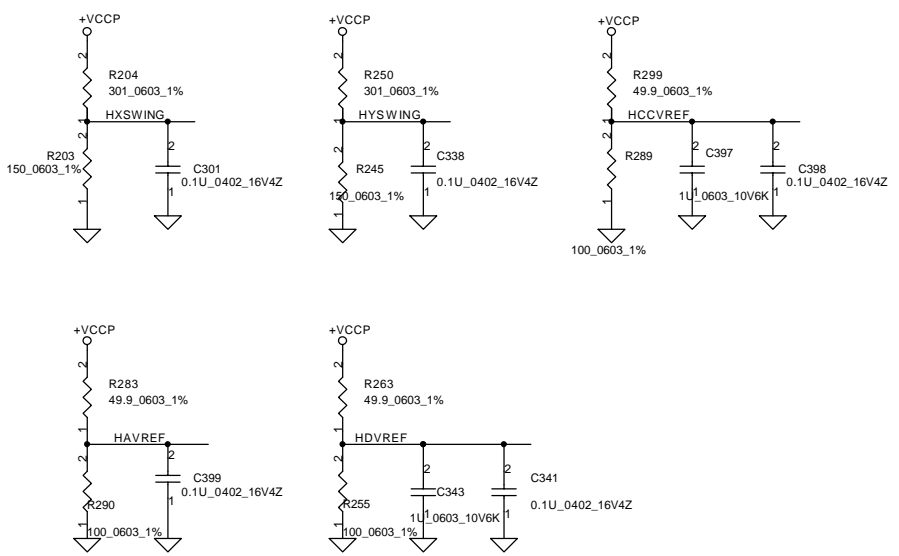
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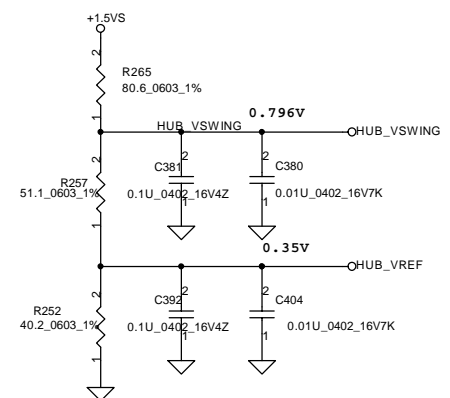
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HOST REF VOLTAGE



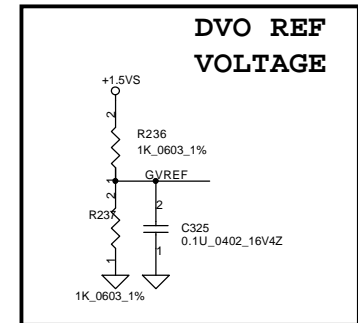
HUB I/F REF VOLTAGE



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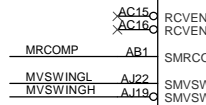
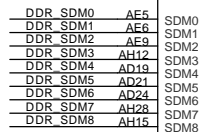
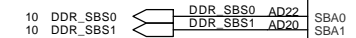
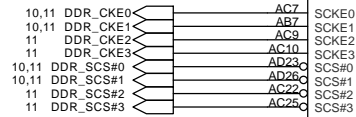
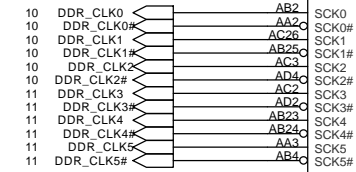
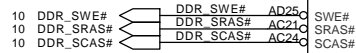
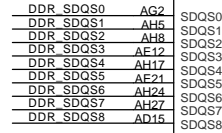
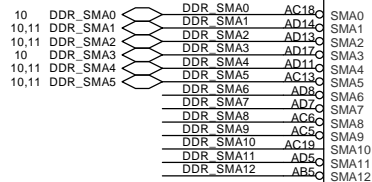
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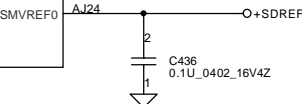
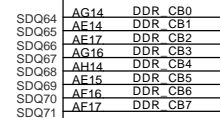
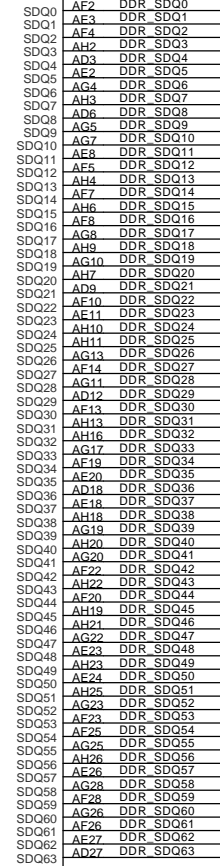
U12C

Montara-GM(L)

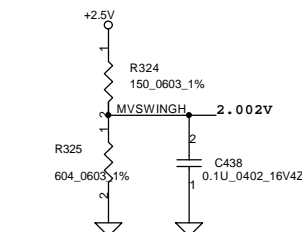
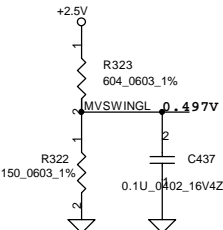
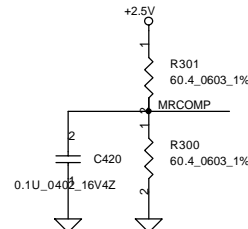


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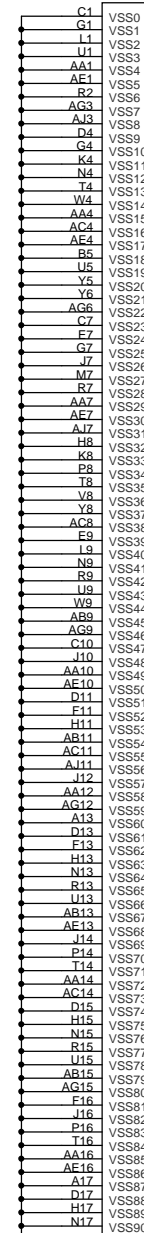
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DDR REF & SWING VOLTAGE

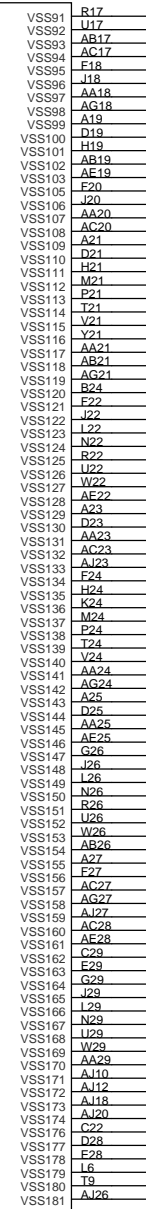


U12D



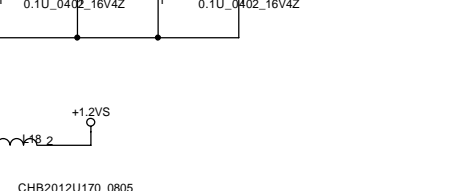
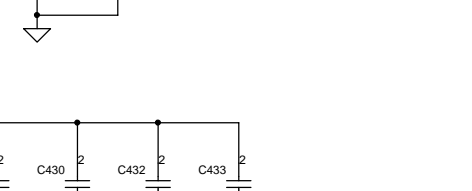
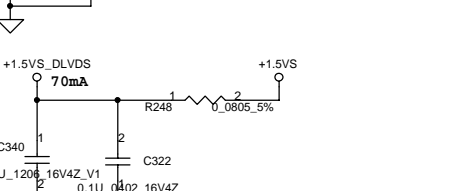
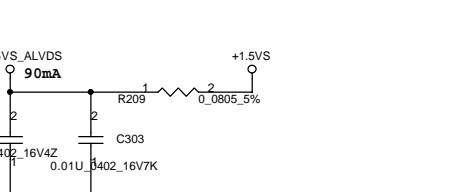
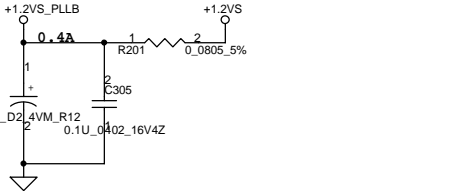
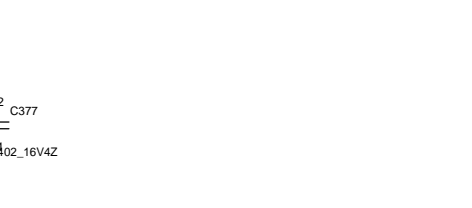
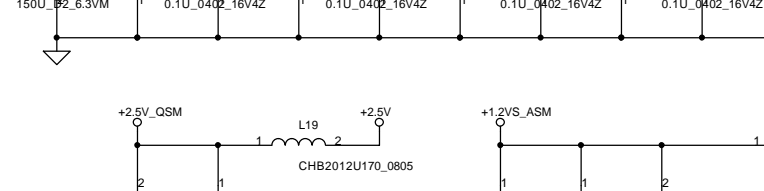
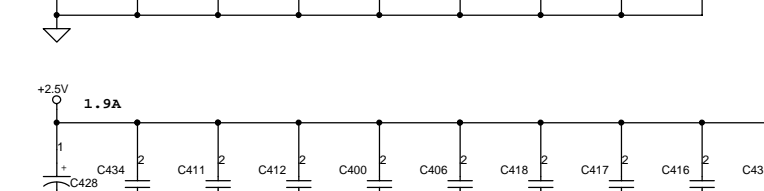
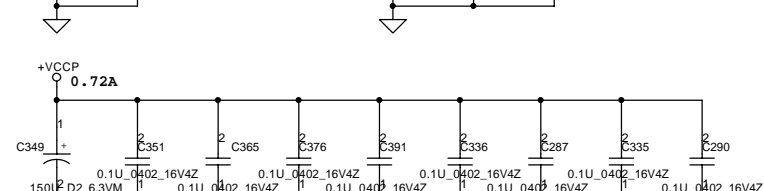
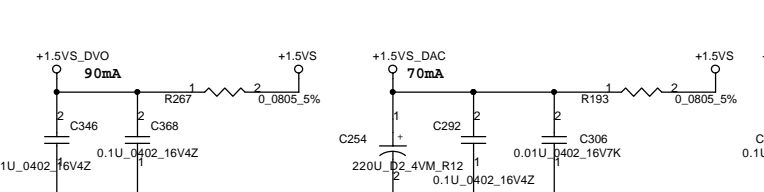
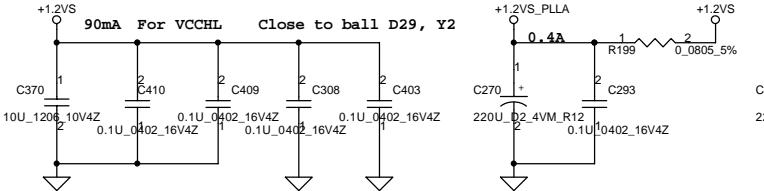
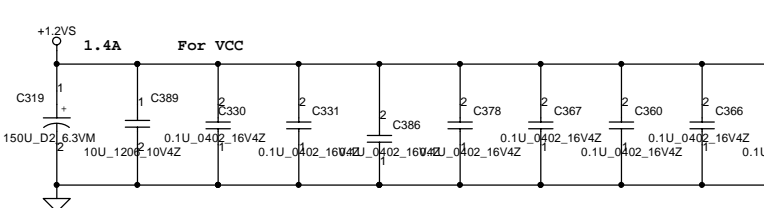
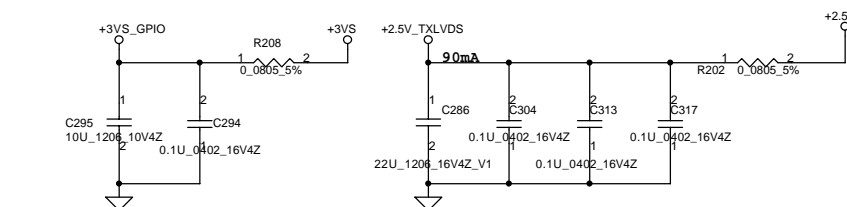
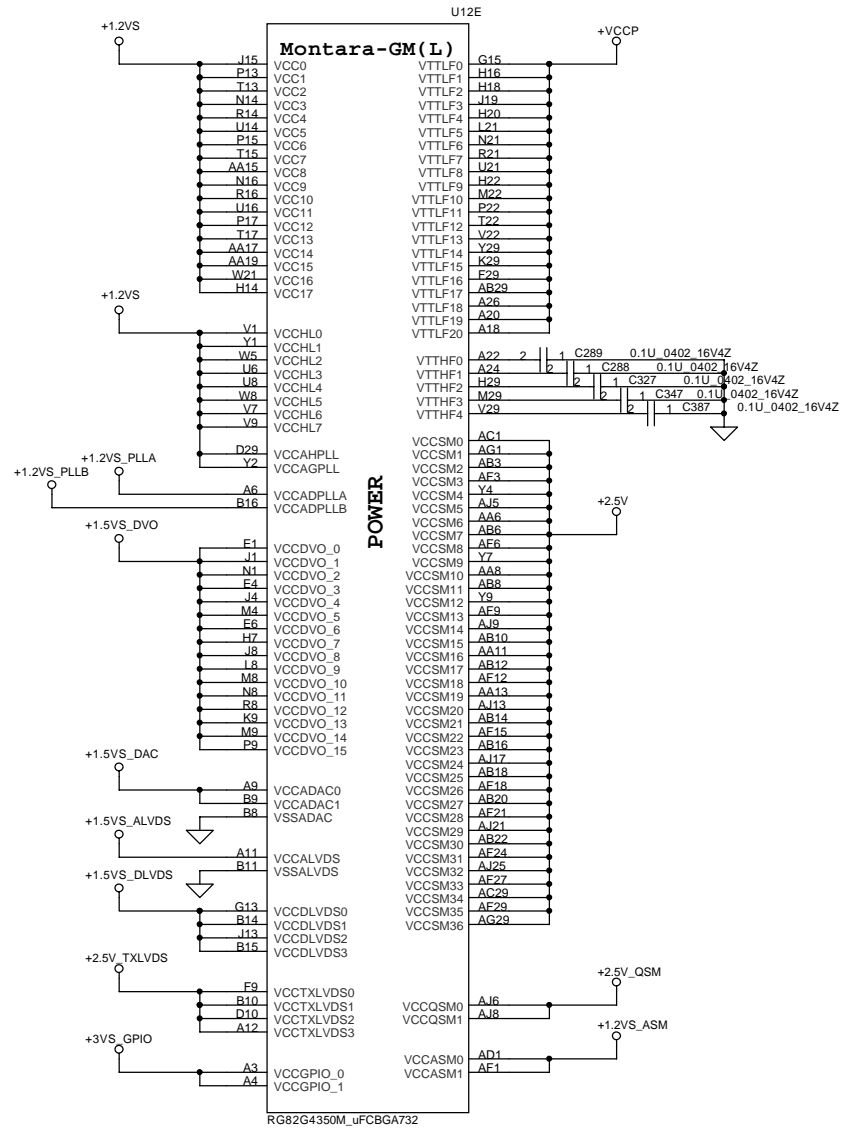
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Montara-GM(L)



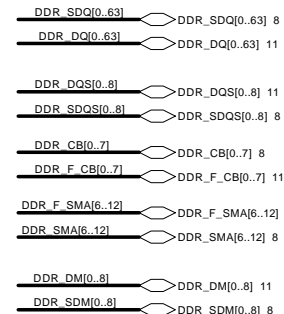
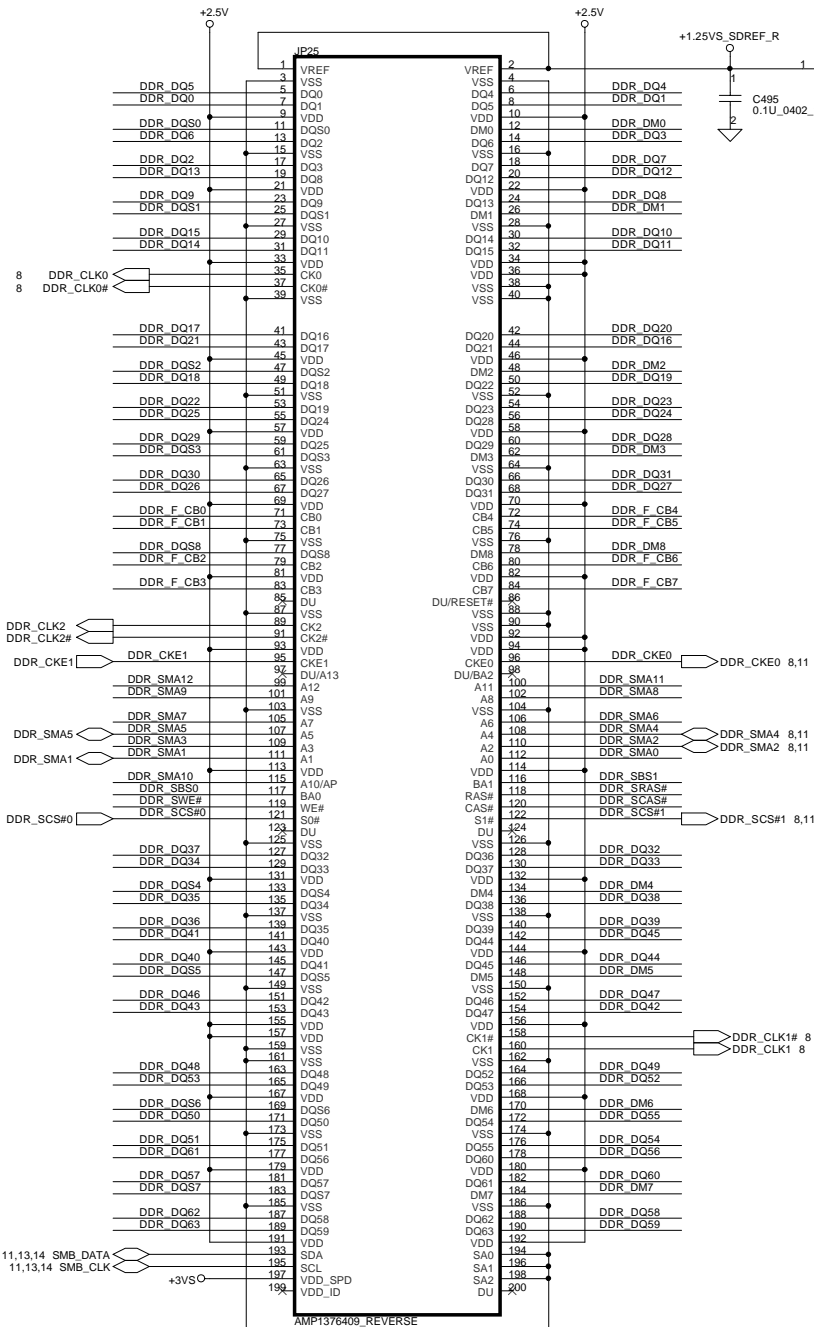
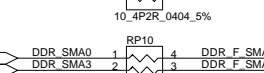
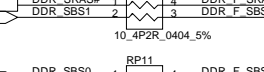
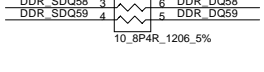
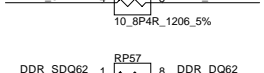
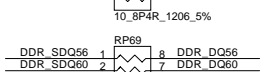
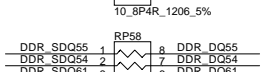
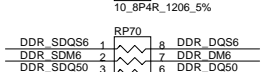
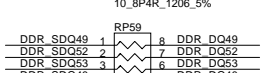
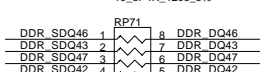
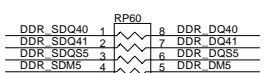
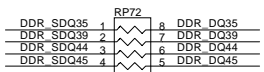
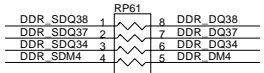
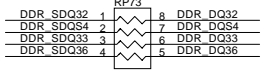
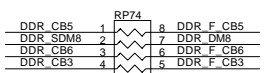
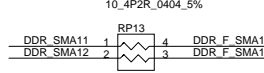
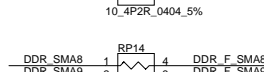
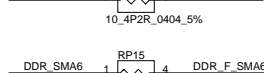
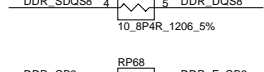
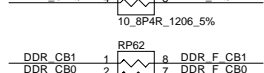
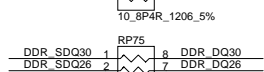
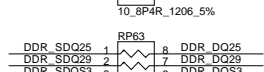
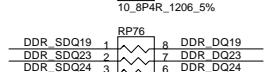
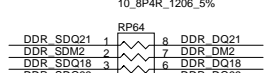
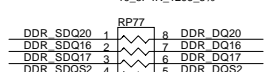
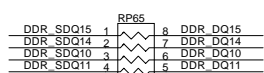
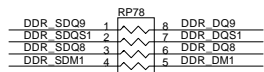
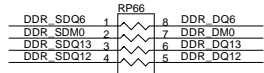
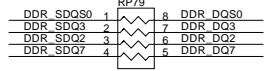
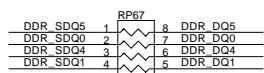
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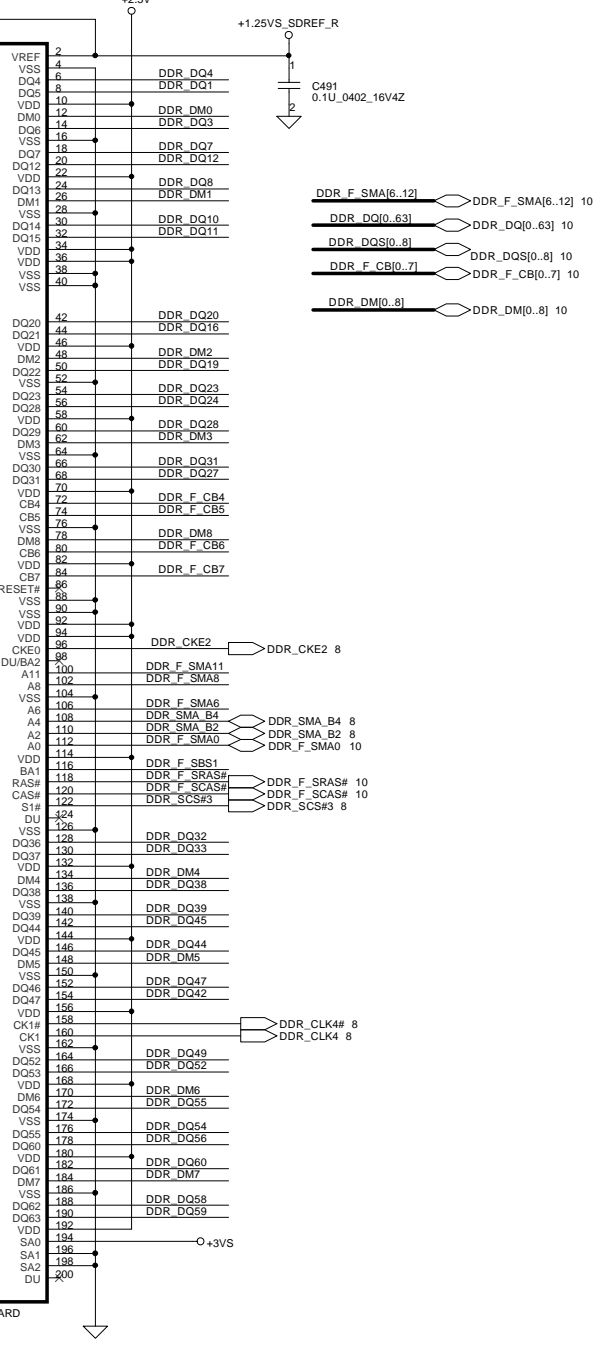
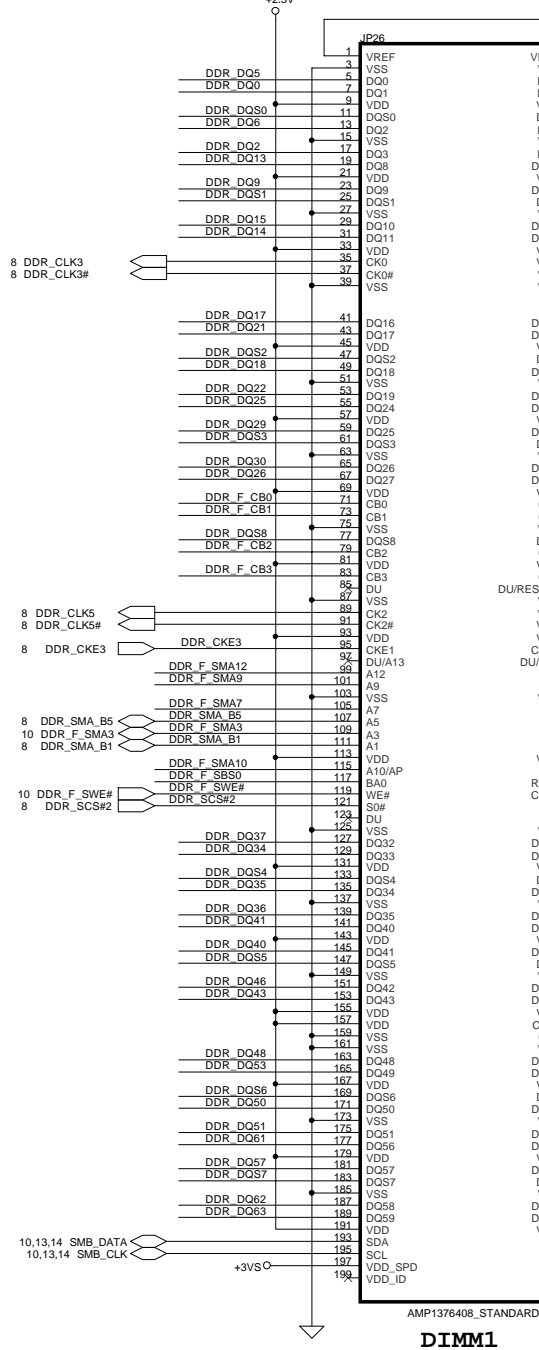
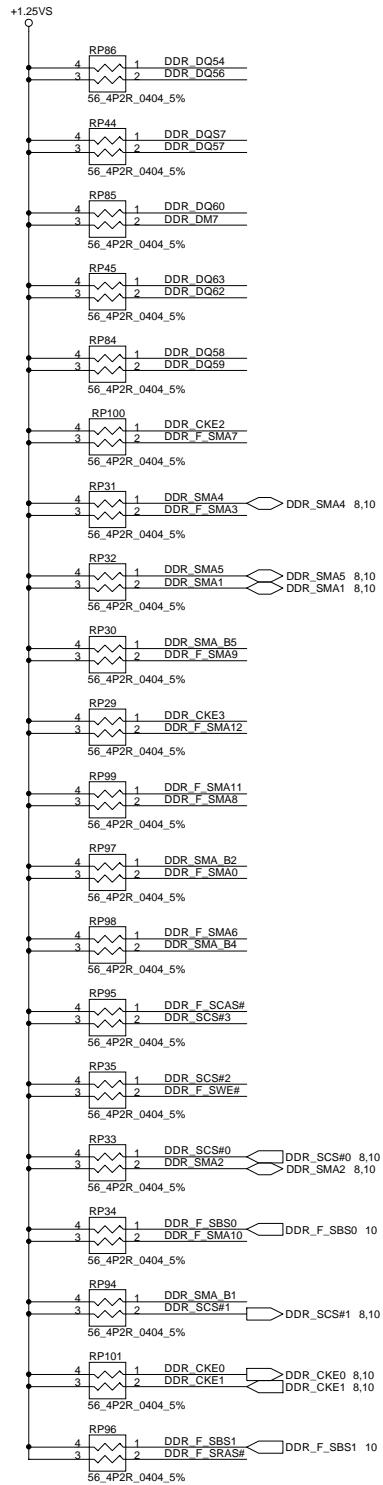
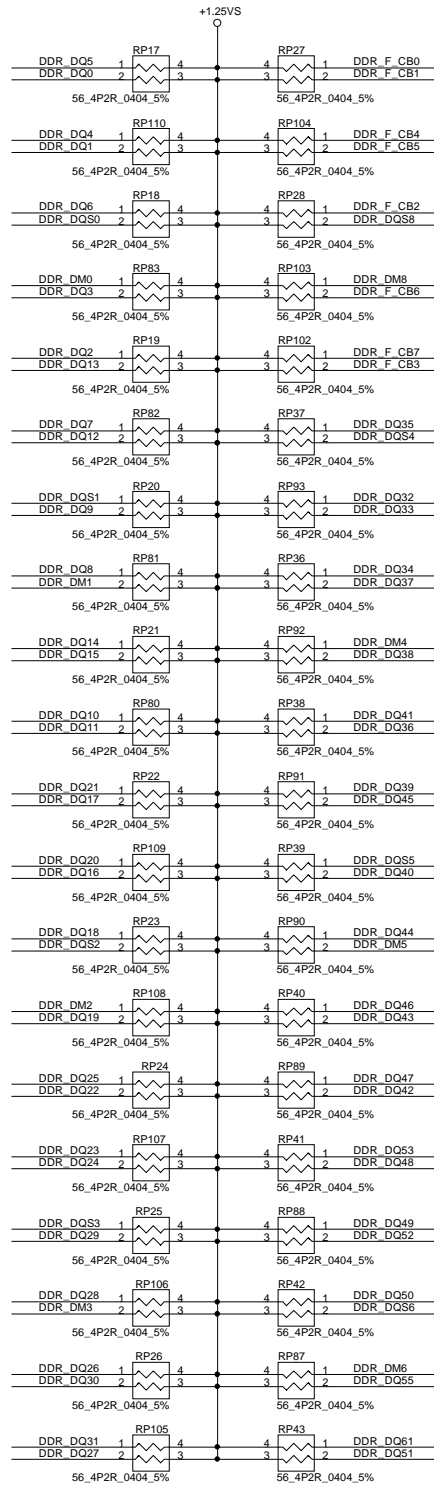
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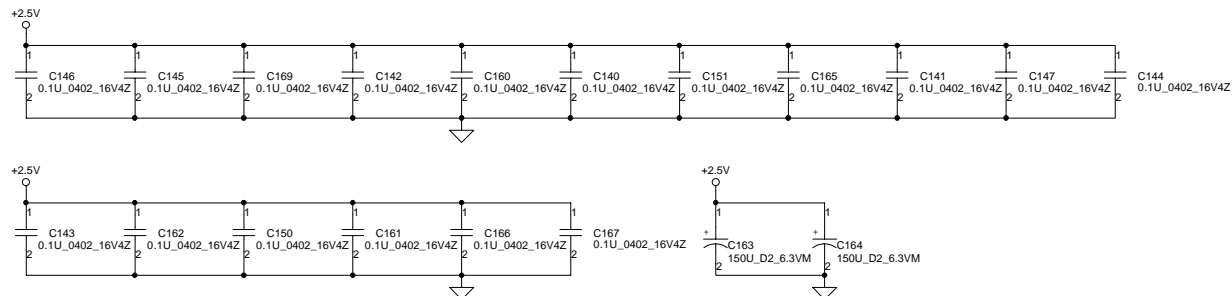


DDR F_SMA[6..12] 10
DDR_DQ[0..63] 10
DDR_DQS[0..8] 10
DDR_F_CB[0..7] 10
DDR_DM[0..8] 10

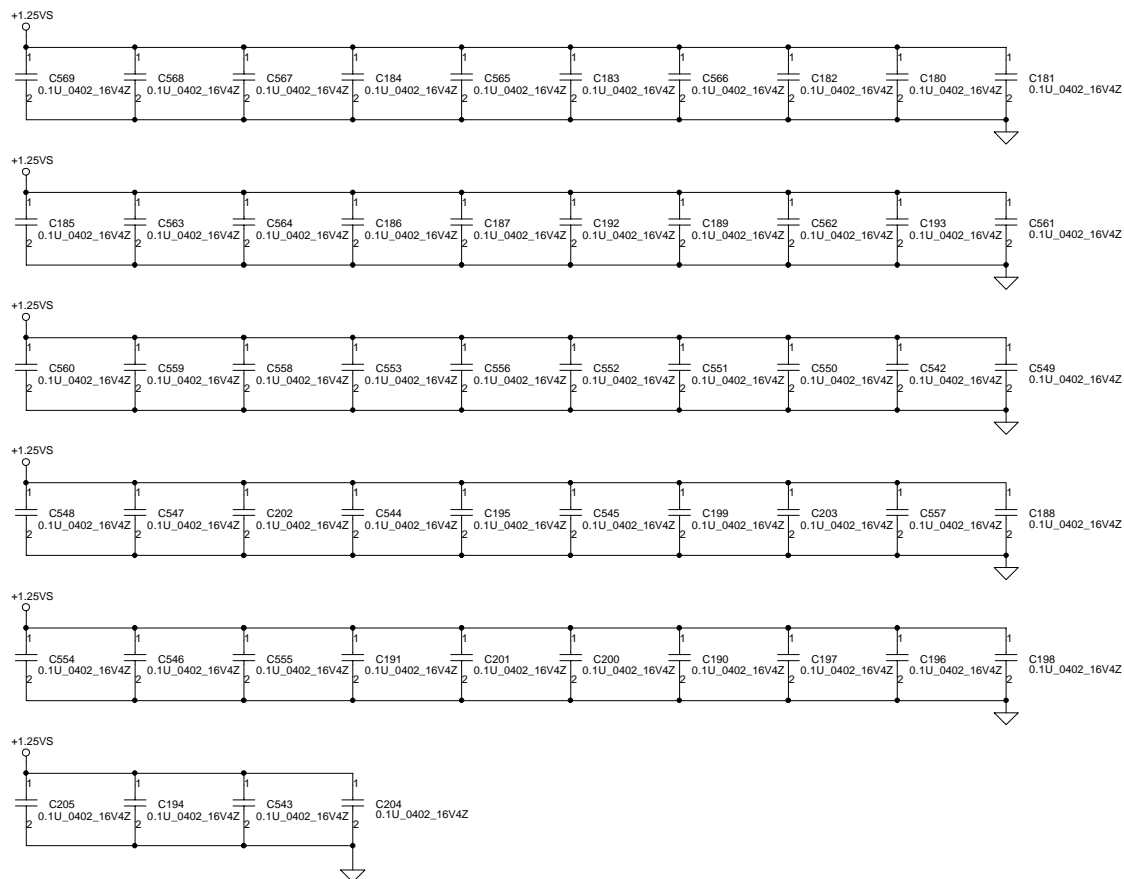
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Layout note :
Distribute as close as possible
to DDR-SODIMM.



Layout note :
Place one cap close to every 2 pull up resistors termination to
+1.25V



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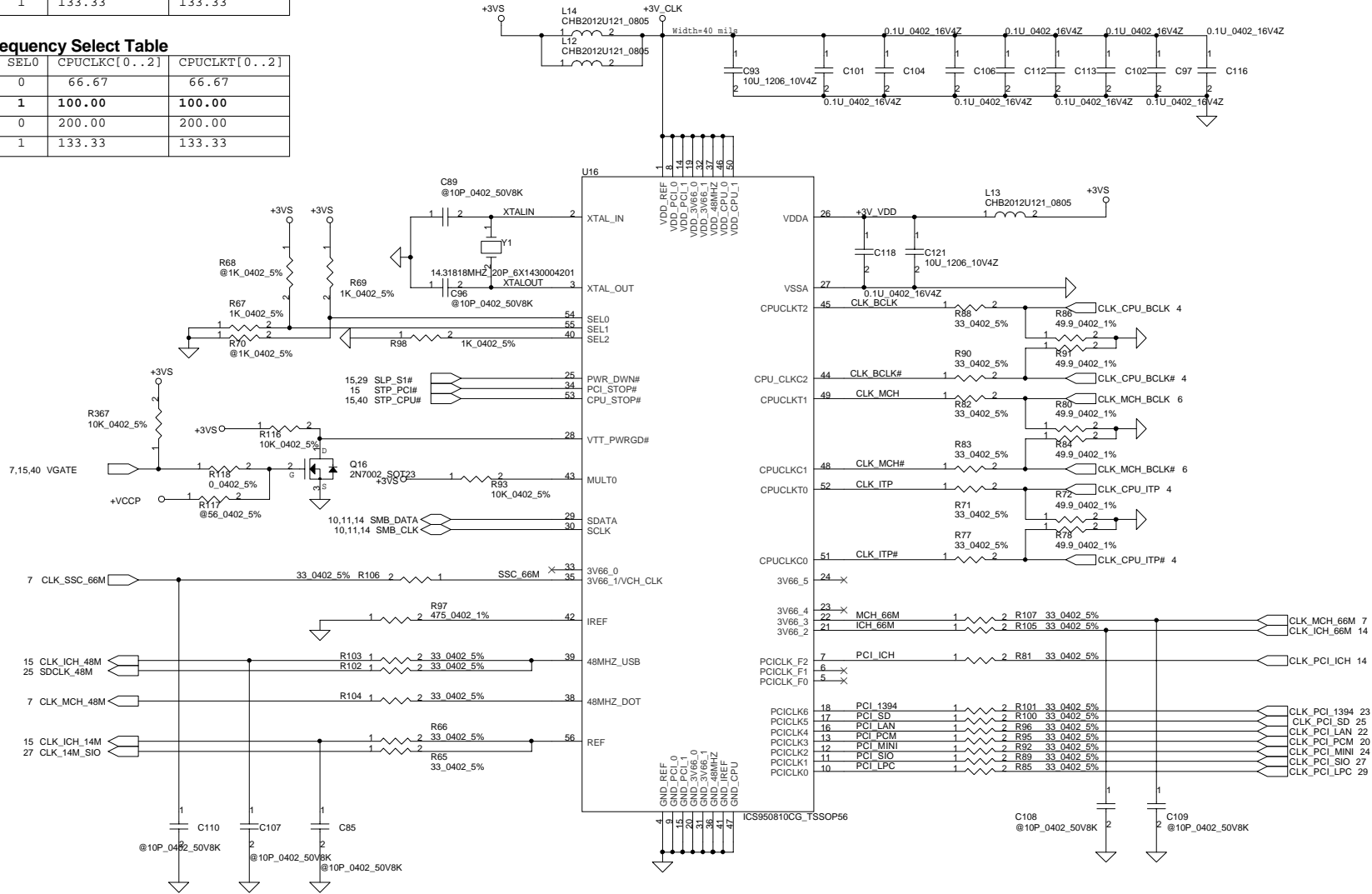
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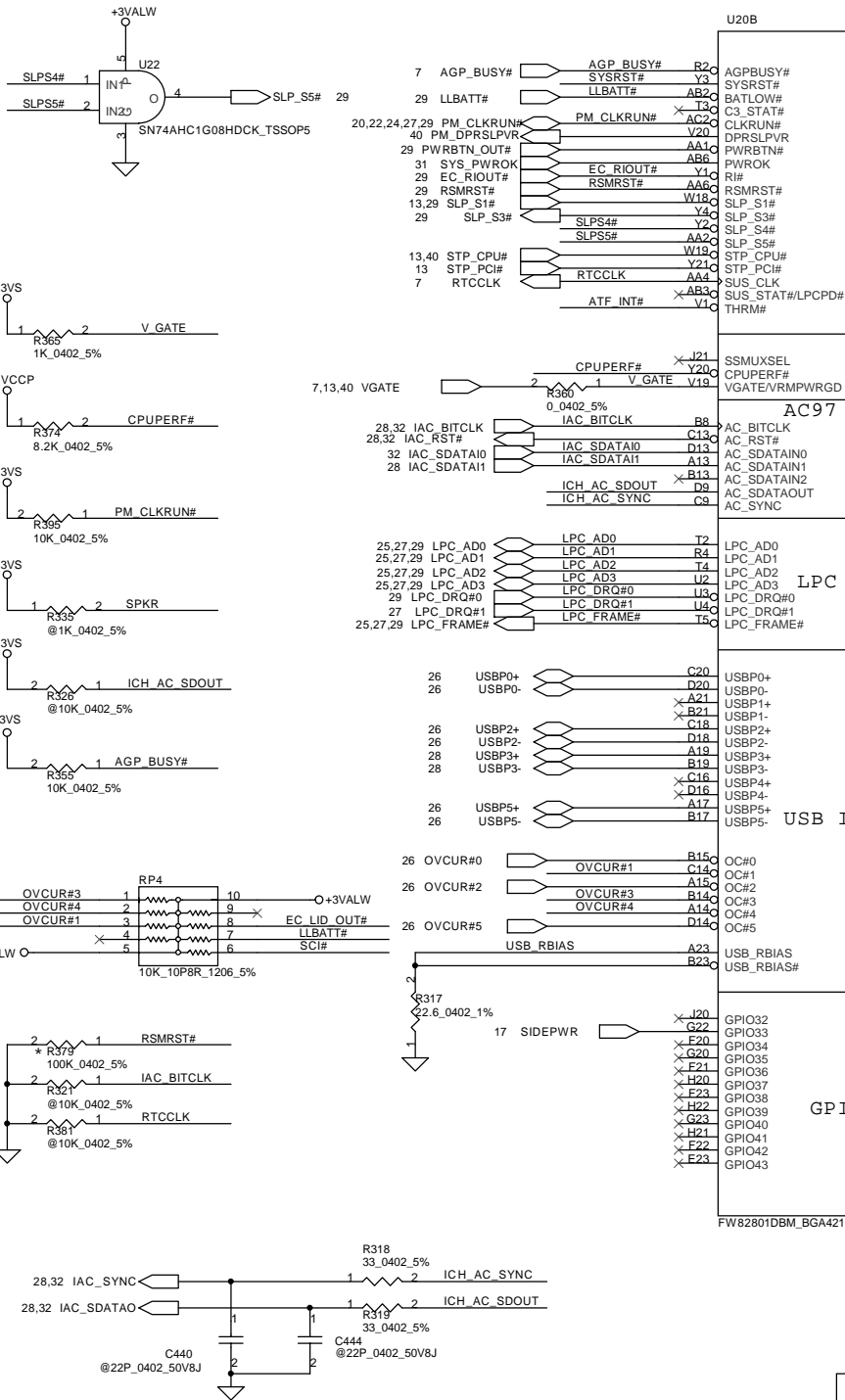
ICS950810 Frequency Select Table

SEL2	SEL1	SEL0	CPUCLKC[0..2]	CPUCLKT[0..2]
0	0	0	166.67	166.67
0	0	1	100.00	100.00
0	1	0	200.00	200.00
0	1	1	133.33	133.33

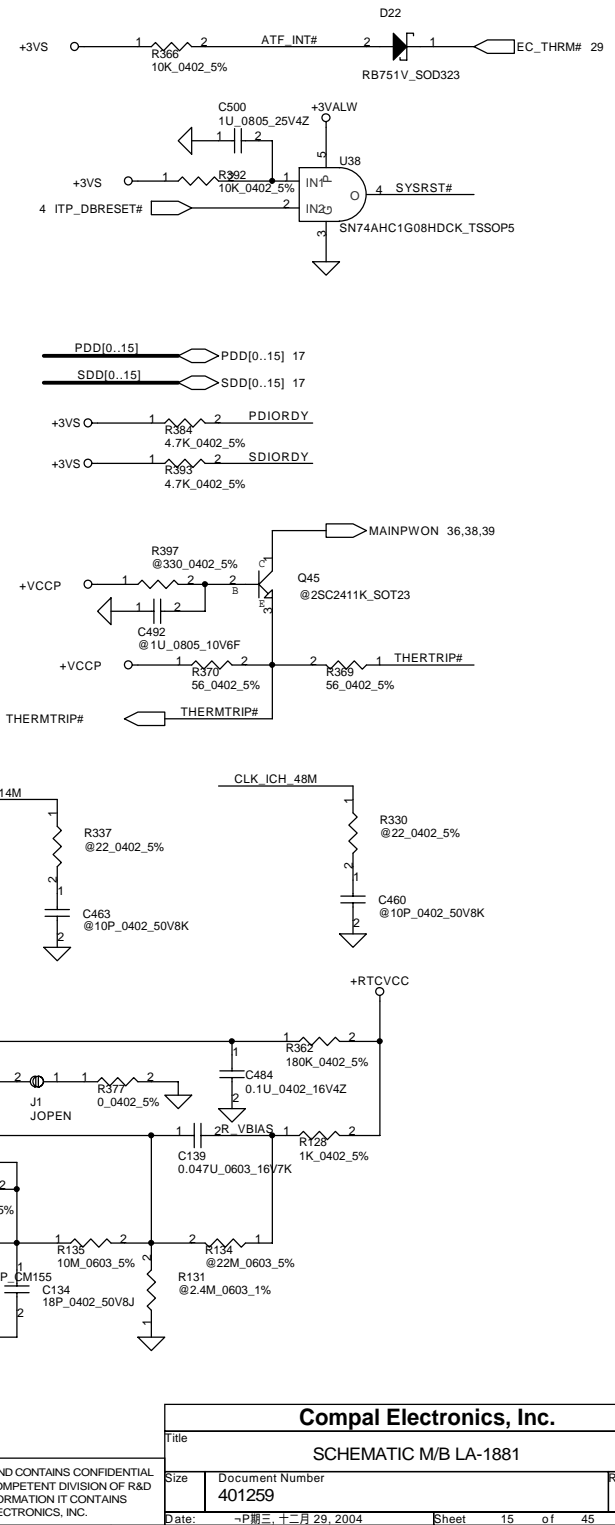
CY28346-2 Frequency Select Table

SEL2	SEL1	SEL0	CPUCLKC[0..2]	CPUCLKT[0..2]
0	0	0	66.67	66.67
0	0	1	100.00	100.00
0	1	0	200.00	200.00
0	1	1	133.33	133.33



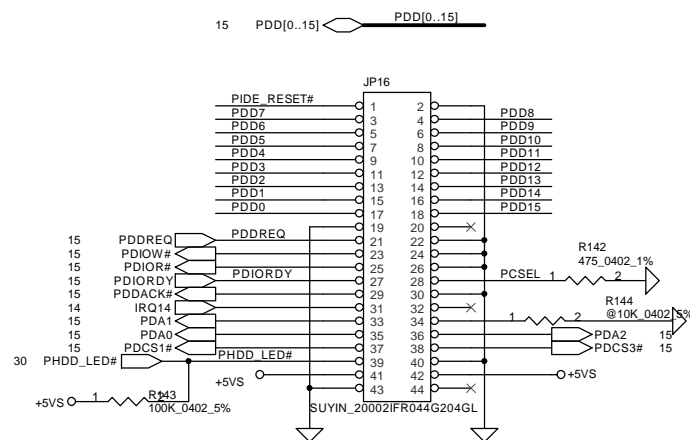


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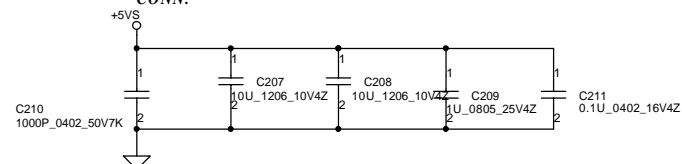


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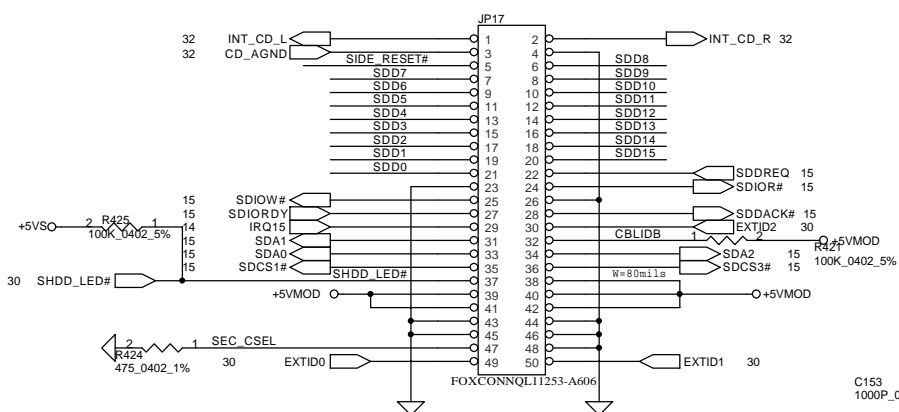
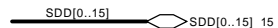
HDD Connector



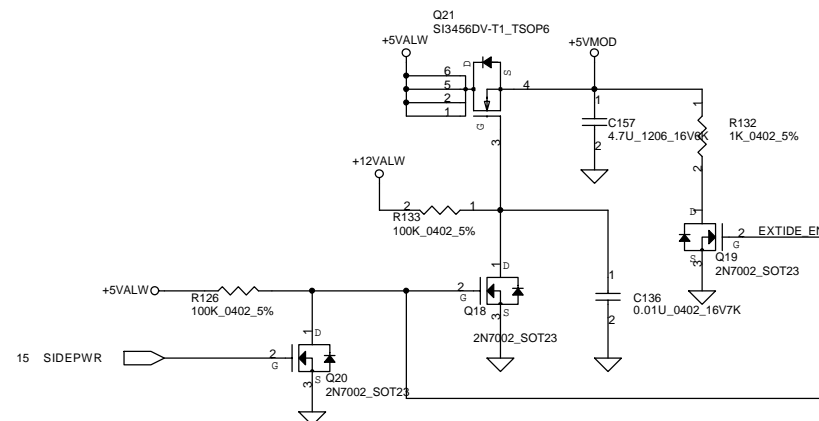
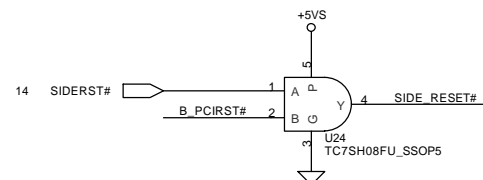
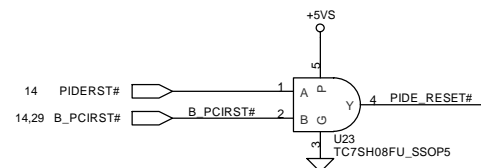
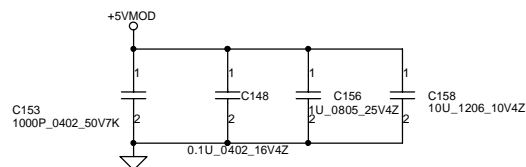
Placea caps. near HDD
CONN.



CD-ROM Connector

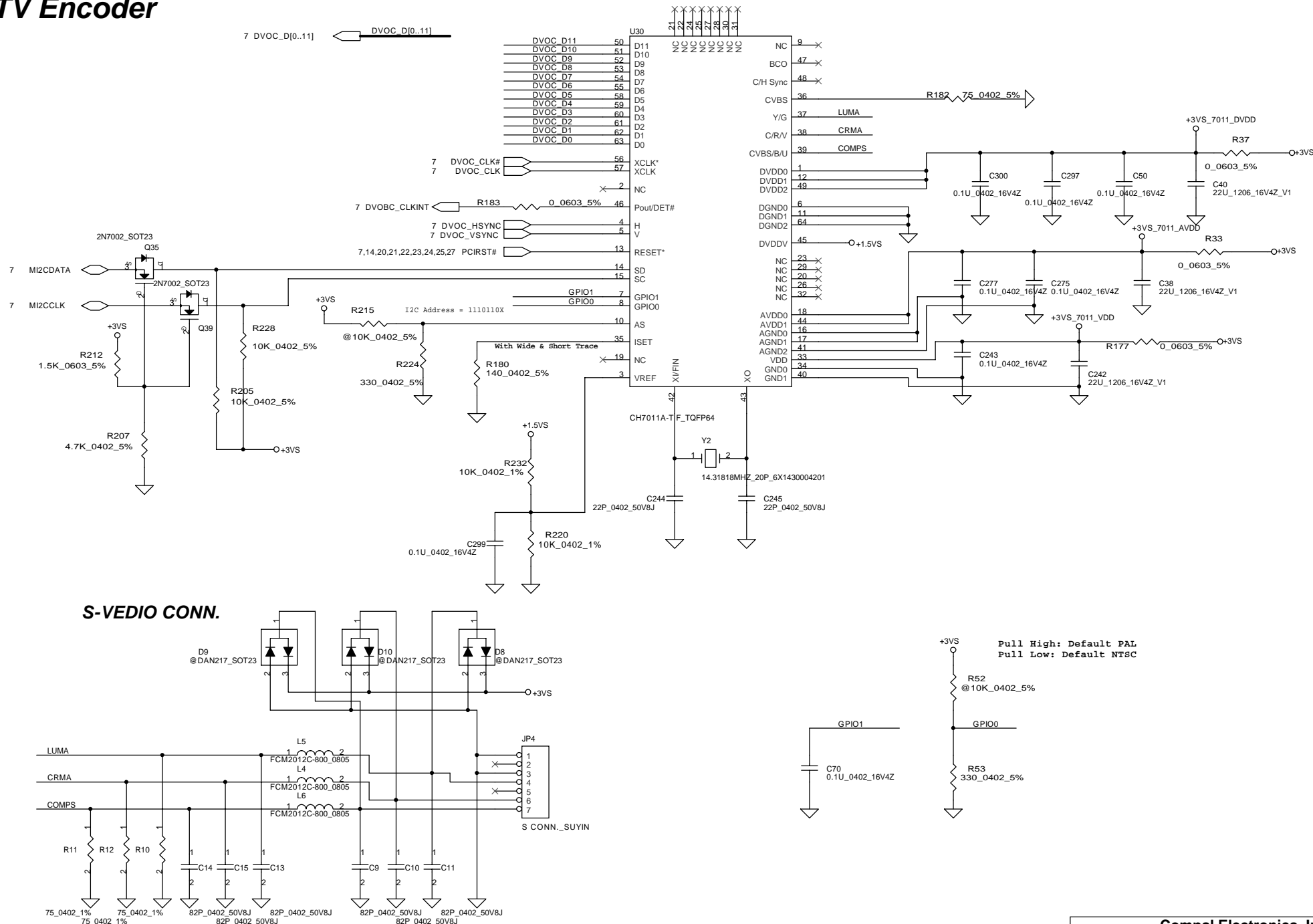


*Placea caps. near CDROM
CONN.*

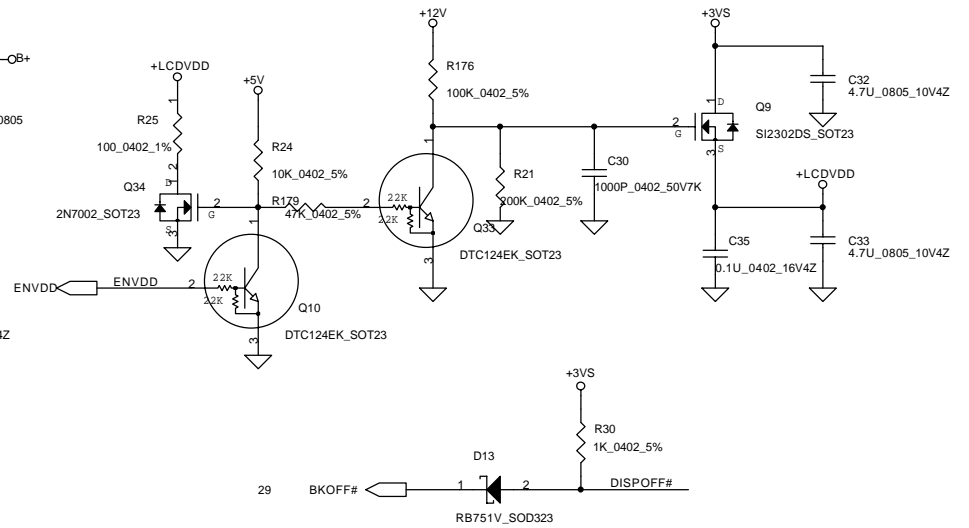
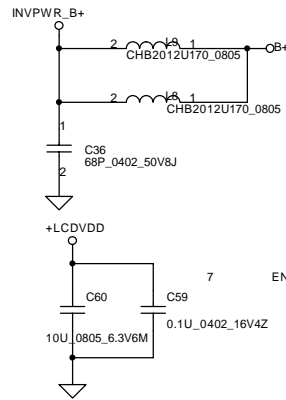
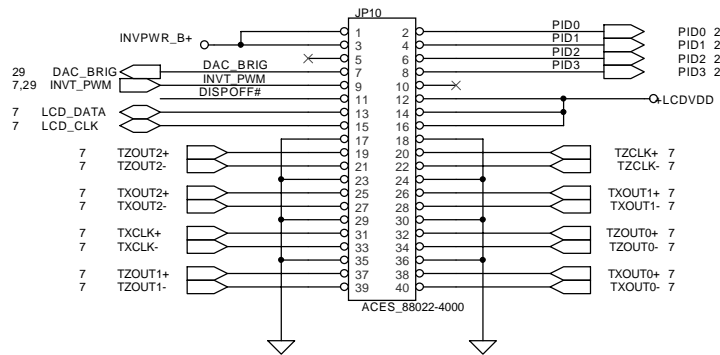


SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS,+20V

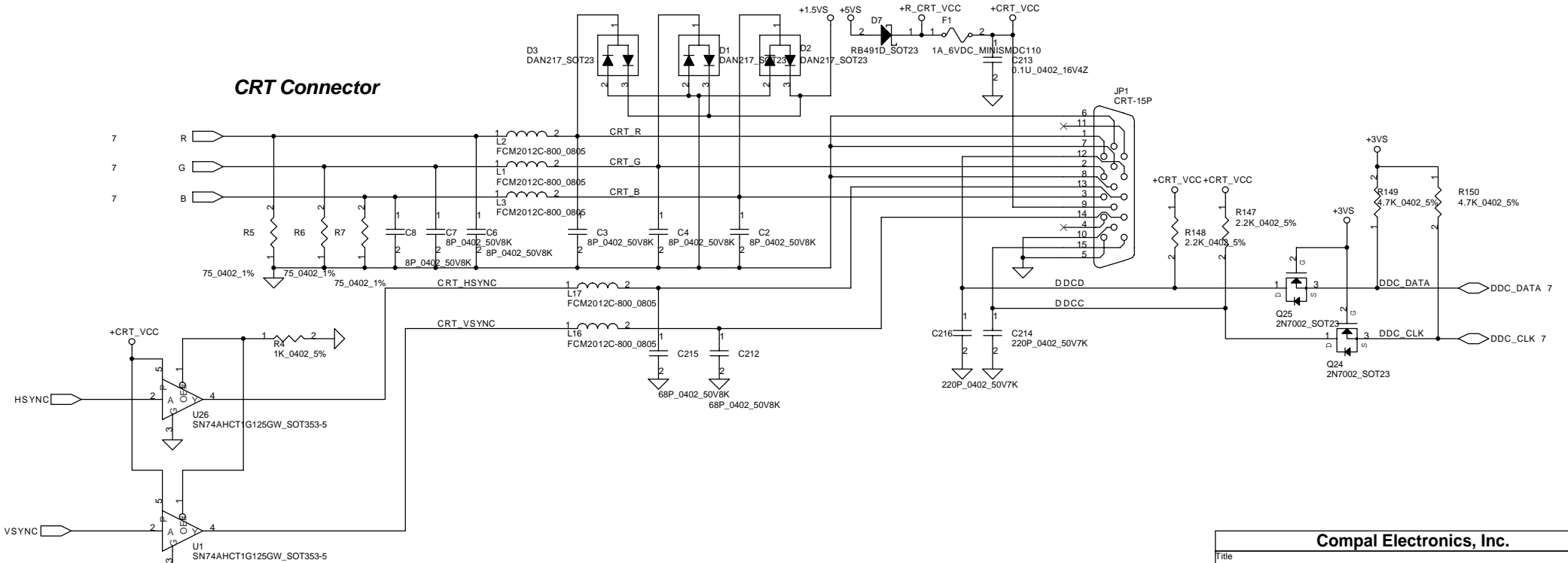
TV Encoder



LVDS Connector



CRT Connector



14,22,23,24 PCI_AD[0..31] PCI_AD[0..31]

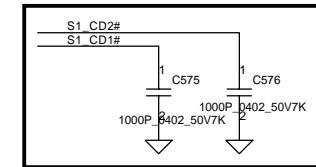
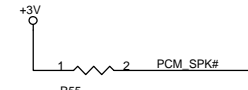
PCI_AD31	3	AD31	144	S1_D10
PCI_AD30	4	AD30	142	S1_D9
PCI_AD29	5	AD29	141	S1_D1
PCI_AD28	7	AD28	140	S1_D8
PCI_AD27	8	AD27	139	S1_D0
PCI_AD26	9	AD26	129	S1_A0
PCI_AD25	10	AD25	128	S1_A1
PCI_AD24	11	AD24	127	S1_A2
PCI_AD23	15	AD23	124	S1_A3
PCI_AD22	16	AD22	121	S1_A4
PCI_AD21	17	AD21	120	S1_A5
PCI_AD20	19	AD20	118	S1_A6
PCI_AD19	23	AD19	116	S1_A25
PCI_AD18	24	AD18	115	S1_A7
PCI_AD17	25	AD17	113	S1_A24
PCI_AD16	26	AD16	98	S1_A17
PCI_AD15	38	AD15	96	S1_IOWR#
PCI_AD14	39	AD14	97	S1_A9
PCI_AD13	40	AD13	93	S1_IORD#
PCI_AD12	41	AD12	95	S1_A11
PCI_AD11	42	AD11	92	S1_OE#
PCI_AD10	45	AD10	91	S1_CE2#
PCI_AD9	46	AD9	89	S1_A10
PCI_AD8	47	AD8	87	S1_D15
PCI_AD7	49	AD7	85	S1_D7
PCI_AD6	51	AD6	82	S1_D13
PCI_AD5	53	AD5	83	S1_D6
PCI_AD4	53	AD4	80	S1_D12
PCI_AD3	54	AD3	81	S1_D5
PCI_AD2	55	AD2	77	S1_D11
PCI_AD1	56	AD1	79	S1_D4
PCI_AD0	57	AD0	76	S1_D3

PQFP 144
22.2 X 22.2 X 1.60

14,22,23,24 PCI_C/BE#3	12	C/BE#3
14,22,23,24 PCI_C/BE#2	27	C/BE#2
14,22,23,24 PCI_C/BE#1	37	C/BE#1
14,22,23,24 PCI_C/BE#0	48	C/BE#0
7,14,18,21,22,23,24,25,27 PCIRST#	20	RST#
14,22,23,24 PCI_FRAME#	28	FRAME#
14,22,23,24 PCI_IRDY#	29	IRDY#
14,22,23,24 PCI_TRDY#	31	TRDY#
14,22,23,24 PCI_DEVSEL#	32	DEVSEL#
14,22,23,24 PCI_STOP#	33	STOP#
14,22,23,24 PCI_PERR#	34	PERR#
14,22,24 PCI_SERR#	35	SERR#
14,22,23,24 PCI_PAR#	36	PAR#
14 PCI_REQ#2	1	REQ#
14 CLK_GNT#2	1	GNT#
13 CLK_PCI_PCM	21	CLK

CC/BE3#/REG#	125	S1_REG#	21
CC/BE2#/A12	112	S1_A12	
CC/BE1#/A8	99	S1_A8	
CC/BE0#/CE1#	88	S1_CE1#	21
CRST#/RESET	119	S1_RST	21
CFRAME#/A23	111	S1_A23	
CIRDY#/A15	110	S1_A15	
CTRDY#/A22	109	S1_A22	
CDEVSEL#/A21	107	S1_A21	
CSTOP#/A20	105	S1_A20	
CPERR#/A14	104	S1_A14	
CSERR#/WAIT#	133	S1_WAIT#	21
CPAR#/A13	101	S1_A13	
CREQ#/INPACK#	123	S1_INPACK#	21
CGNT#/WE#	106	S1_WE#	21
CLK/A16	108	A16_CLK	1
CSTSCHG/BVD1	135	S1_BVD1	21
CLKRUN#/WP	136	S1_WP	21
CBLOCK#/A19	103	S1_A19	
CINT#/READY	132	S1_RDY#	21
SPKOUT	62	PCM_SPK#	33
CAUDIO/BVD2	134	S1_BVD2	21
CCD2#/CD2#	137	S1_CD2#	21
CCD1#/CD1#	125	S1_CD1#	21
CVS2/VS2#	117	S1_VS2	21
CVS1/VS1#	131	S1_VS1	21

S1_A[0..25] S1_A[0..25] 21
S1_D[0..15] S1_D[0..15] 21



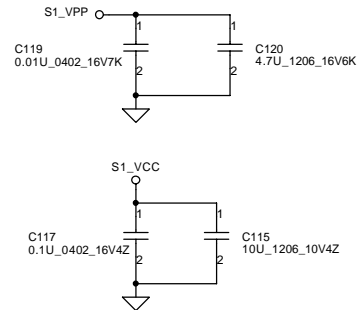
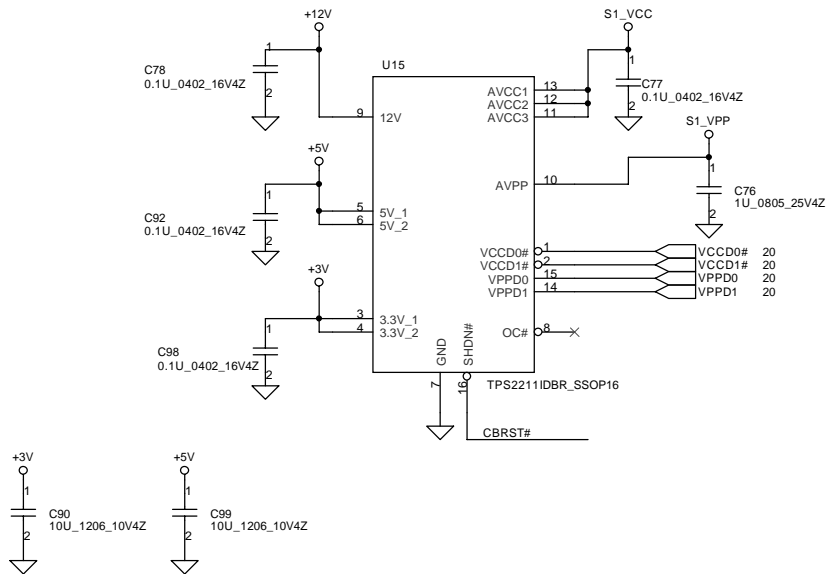
Reserve for CB1410 B0 Version

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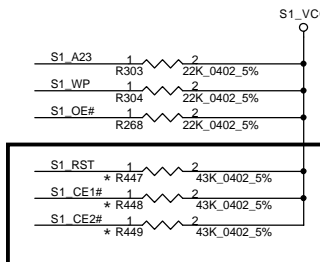
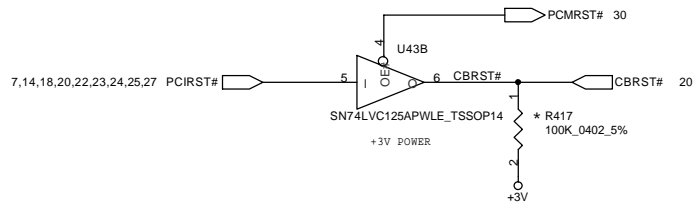
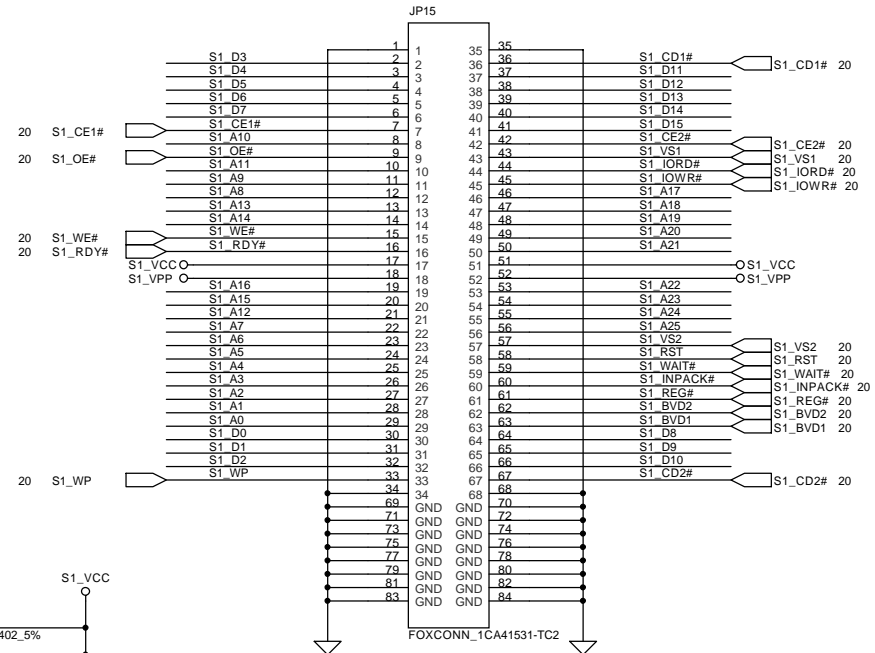
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PCMCIA Power Controller



CardBus Socket



Reserve for CB1410 B0 Version

14,20,23,24 PCI_AD[0..31]

PCI AD0	47	AD0
PCI AD1	46	AD1
PCI AD2	45	AD2
PCI AD3	43	AD3
PCI AD4	42	AD4
PCI AD5	41	AD5
PCI AD6	40	AD6
PCI AD7	39	AD7
PCI AD8	36	AD8
PCI AD9	35	AD9
PCI AD10	34	AD10
PCI AD11	33	AD11
PCI AD12	32	AD12
PCI AD13	30	AD13
PCI AD14	29	AD14
PCI AD15	28	AD15
PCI AD16	15	AD16
PCI AD17	14	AD17
PCI AD18	13	AD18
PCI AD19	12	AD19
PCI AD20	11	AD20
PCI AD21	10	AD21
PCI AD22	9	AD22
PCI AD23	8	AD23
PCI AD24	96	AD24
PCI AD25	93	AD25
PCI AD26	92	AD26
PCI AD27	91	AD27
PCI AD28	89	AD28
PCI AD29	87	AD29
PCI AD30	86	AD30
PCI AD31	85	AD31

Power

PCI I/F

LAN I/F

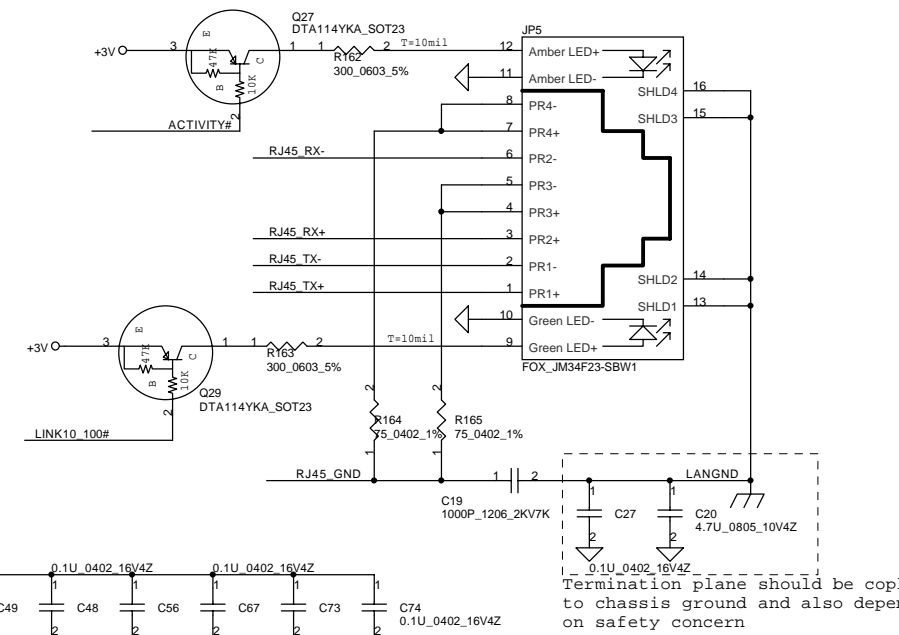
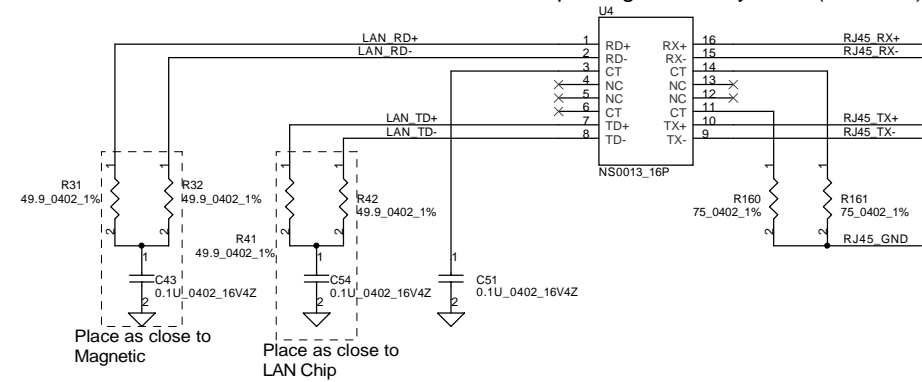
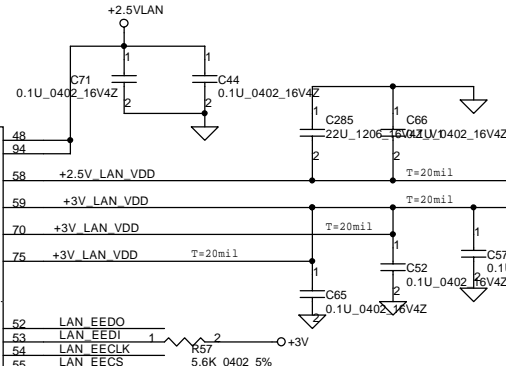
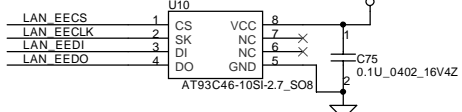
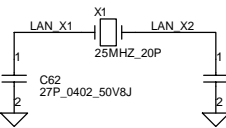
AC-Link

Power

14,20,23,24	PCI_C/BE#0	PCI_C/BE#0	27	C/BE#0
14,20,23,24	PCI_C/BE#1	PCI_C/BE#1	27	C/BE#1
14,20,23,24	PCI_C/BE#2	PCI_C/BE#2	17	C/BE#2
14,20,23,24	PCI_C/BE#3	PCI_C/BE#3	84	C/BE#3
14,20,23,24	PCI_PAR	LAN_IDSEL	98	IDSEL
14,20,23,24	PCI_FRAME#	18	FRAME#	
14,20,23,24	PCI_IRDY#	19	IRDY#	
14,20,23,24	PCI_TRDY#	20	TRDY#	
14,20,23,24	PCI_DEVSEL#	21	DEVSEL#	
14,20,23,24	PCI_STOP#	23	STOP#	
14,20,23,24	PCI_PERR#	25	PERR#	
14,20,23,24	PCI_SERR#	26	SERR#	
14	PCI_REQ#3	83	REQ#	
14	PCI_GNT#3	82	GNT#	
14	PCI_PIRQF#	80	INTA#	
20,23,24,29	LAN_PME#	57	INTB#	
7,14,18,20,21,23,24,25,27	PCIRST#	81	PME#	
13	CLK_PCI_LAN	87	RST#	
15,20,24,27,29	PM_CLKRUN#	50	PCICLK	
			CLKRUN#	

6	VDD	16	DGND1
22	VDD	31	DGND2
37	VDD	44	DGND3
49	VDD	88	DGND4
90	VDD	88	DGND5
95	VDD	62	AGND1
		66	AGND2
		73	AGND3

RTL8101L_LQFP100



Note : I_{max} for VDD25 = 40mA
2.5V should be ready before
+3V is ready

2.5VLAN power generated by VCTRL(RT8100BL)
2.5VLAN power generated by PIN58(RT8101L)

Place as close to
Magnetic

Place as close to
LAN Chip

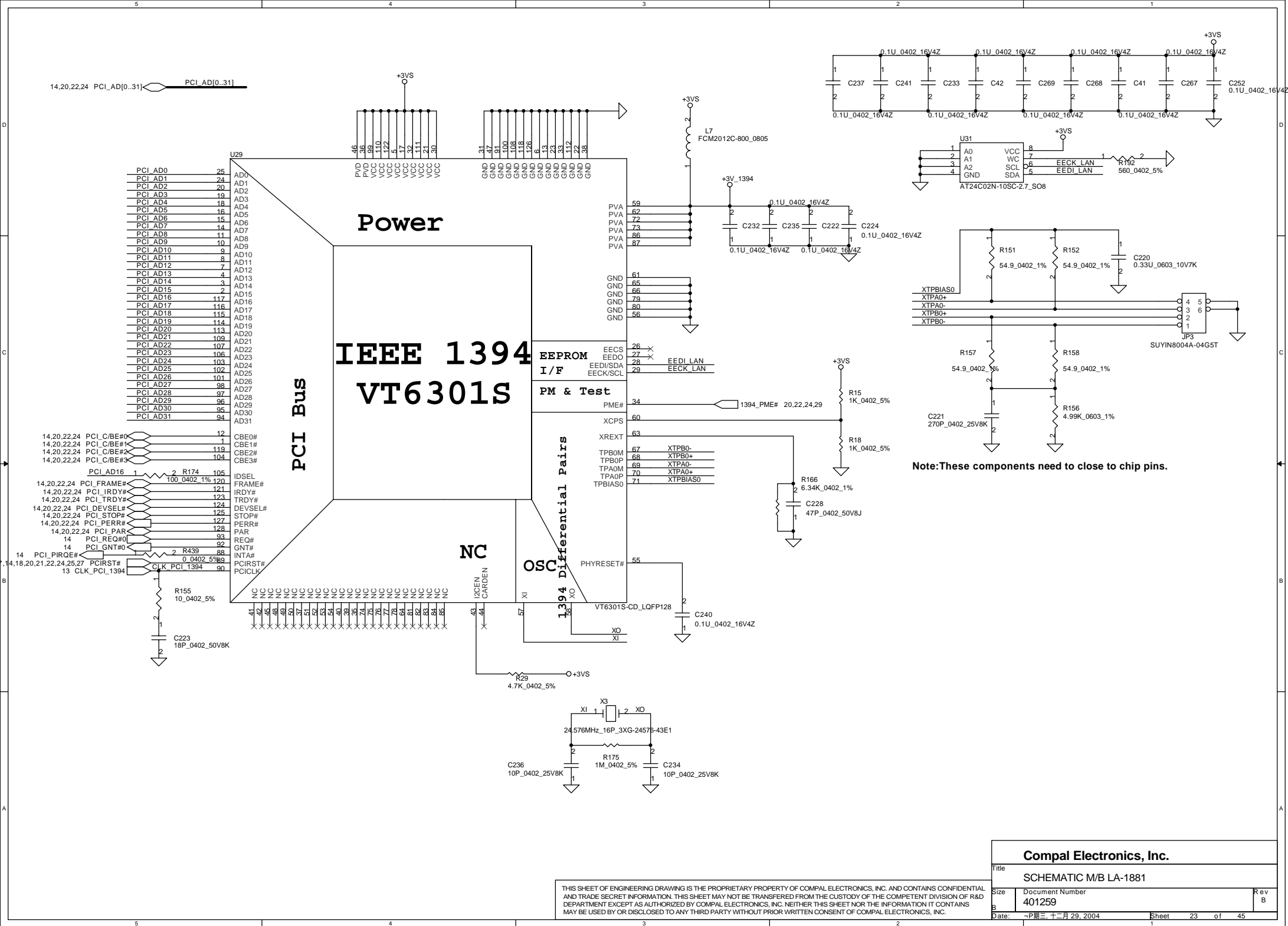
Termination plane should be copied
to chassis ground and also depends
on safety concern

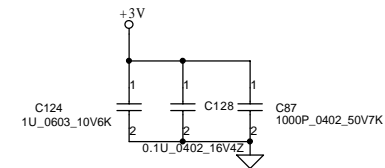
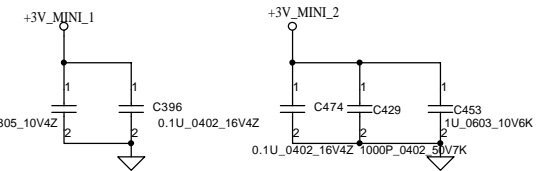
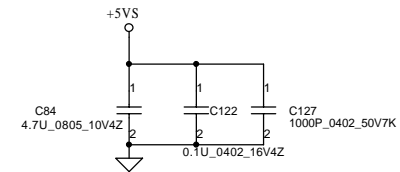
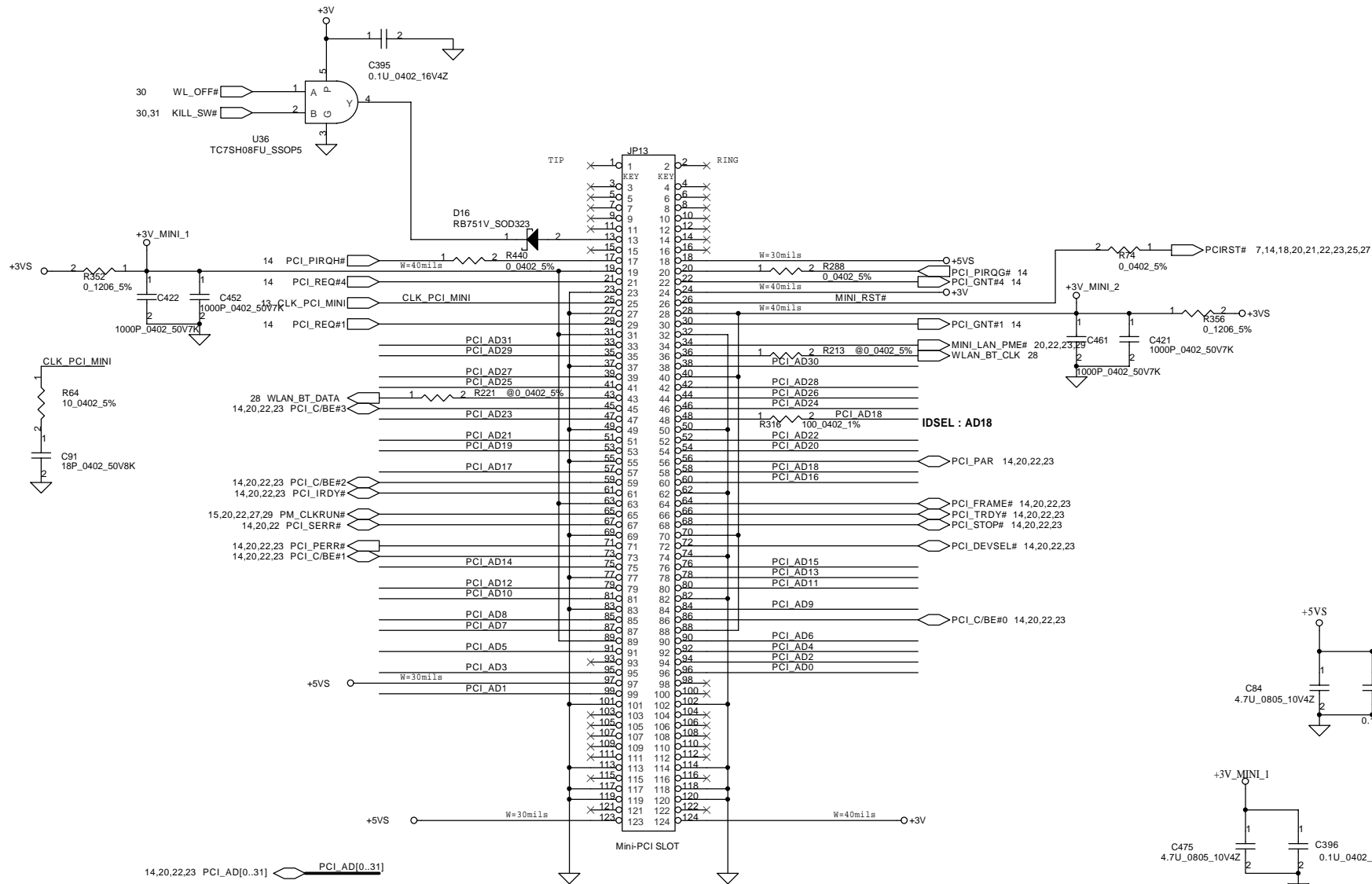
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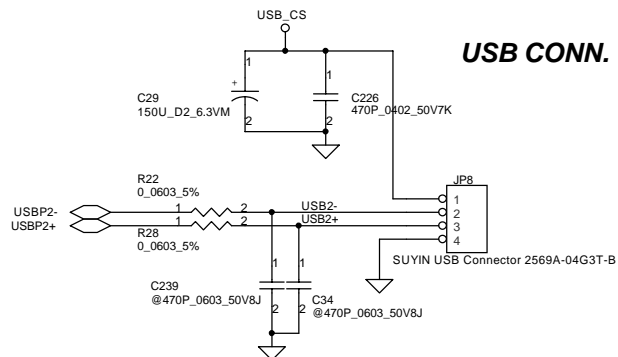
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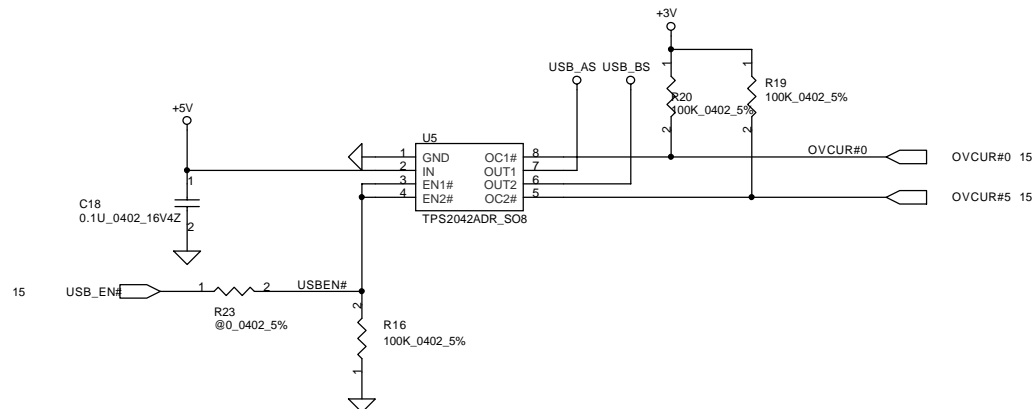
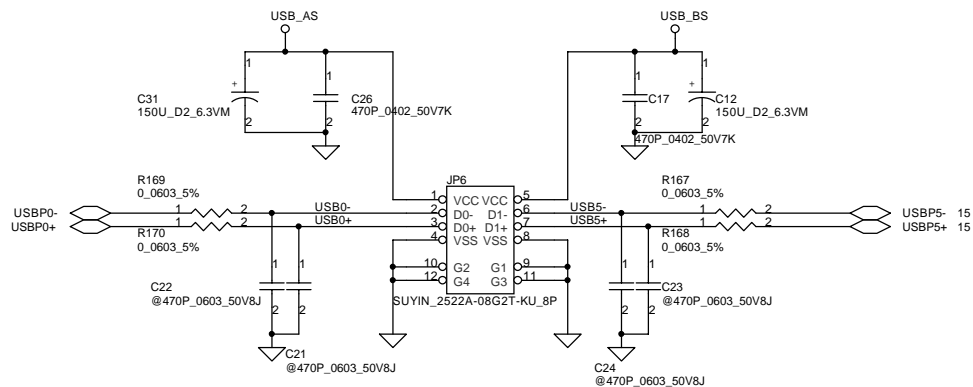
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Rev
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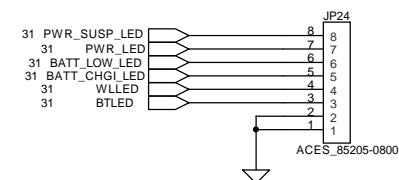
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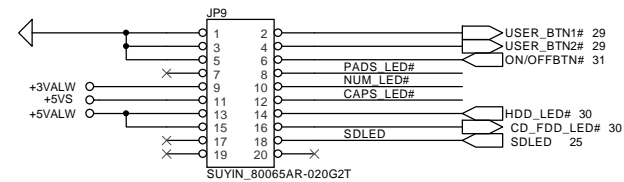
USB CONN.



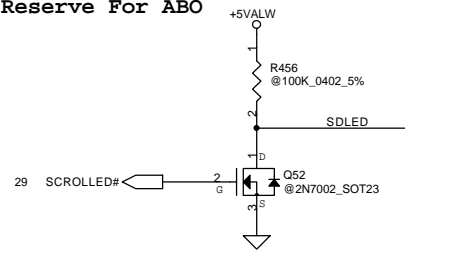
LED/B CONN.



POWER/B CONN.



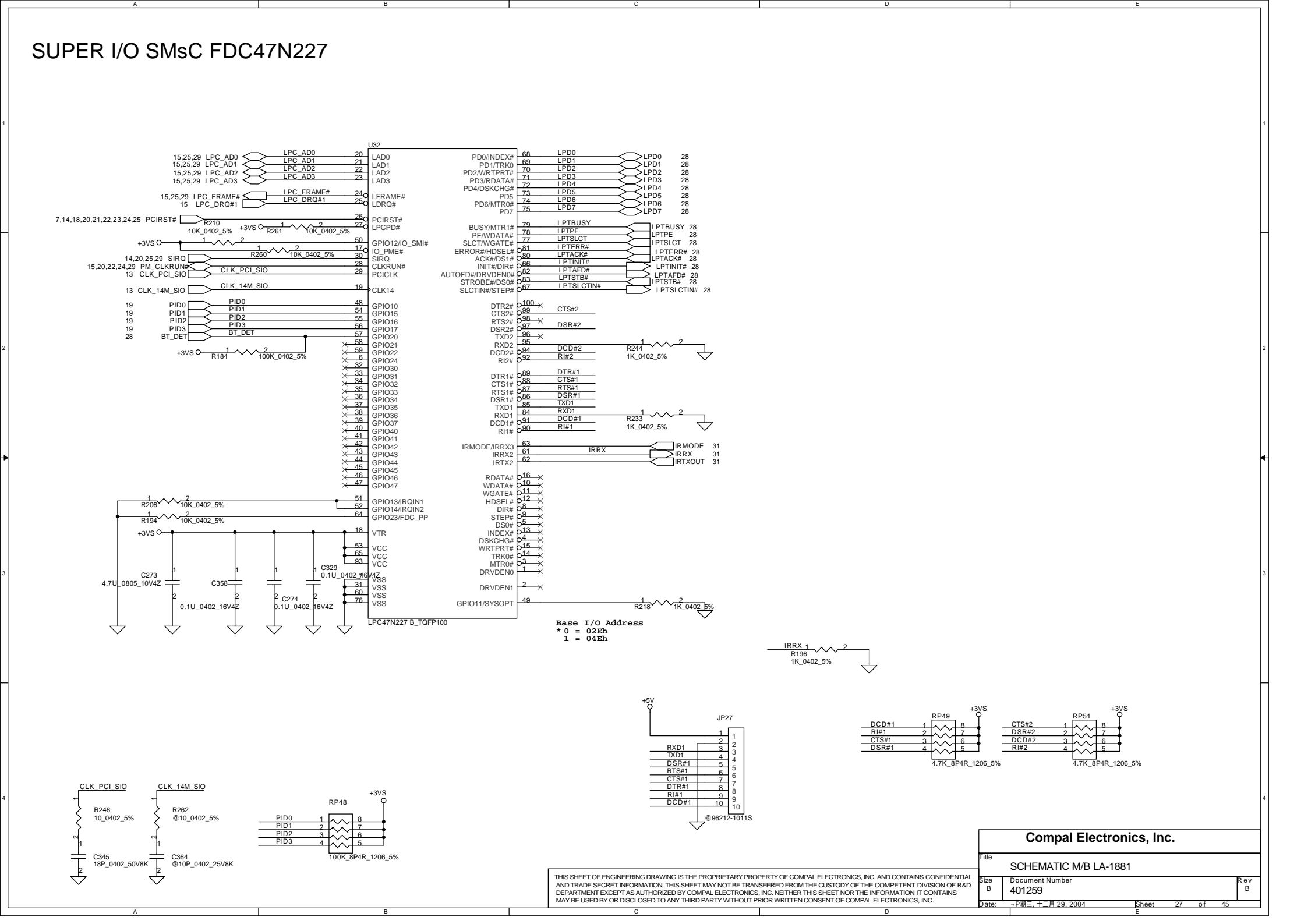
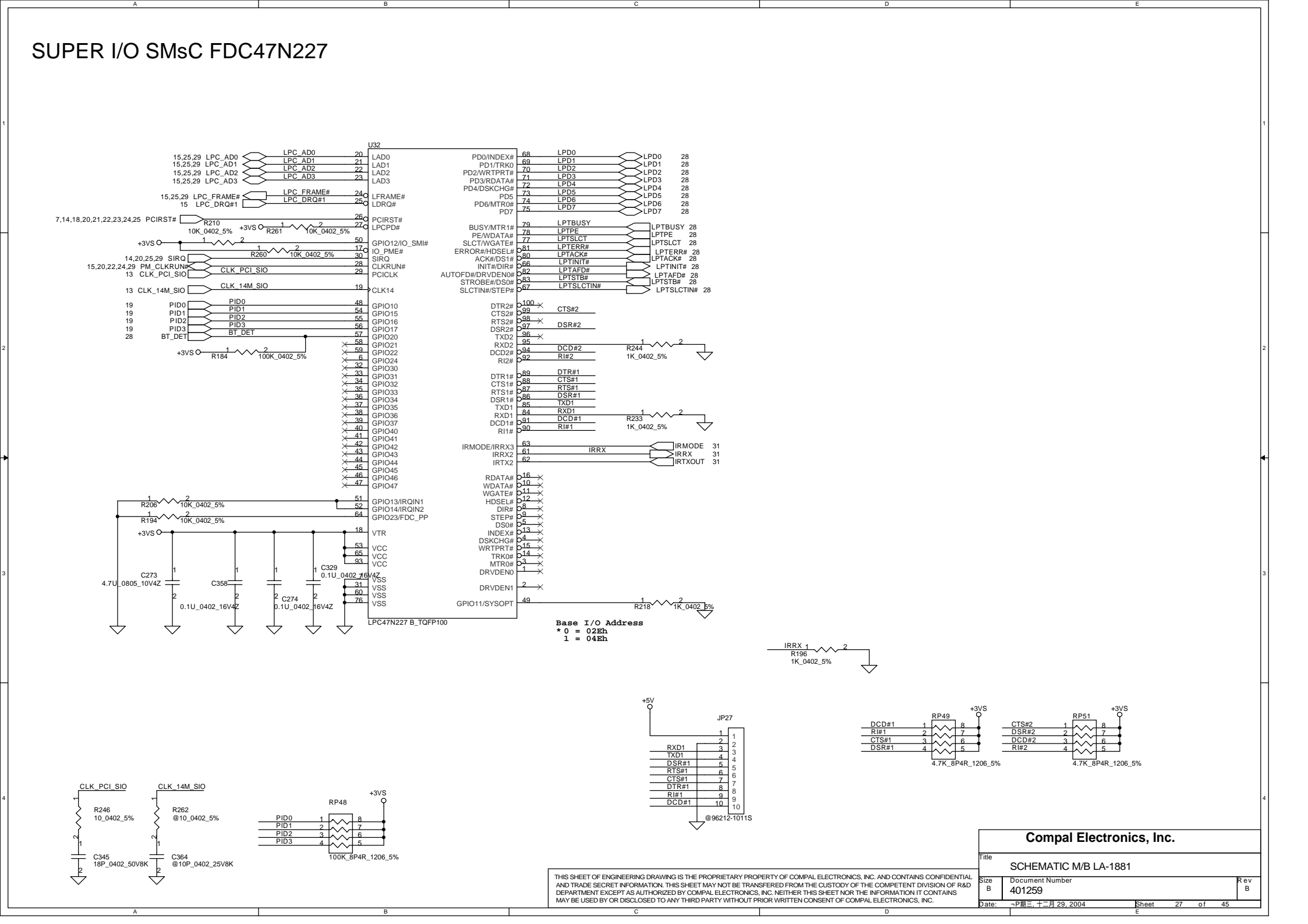
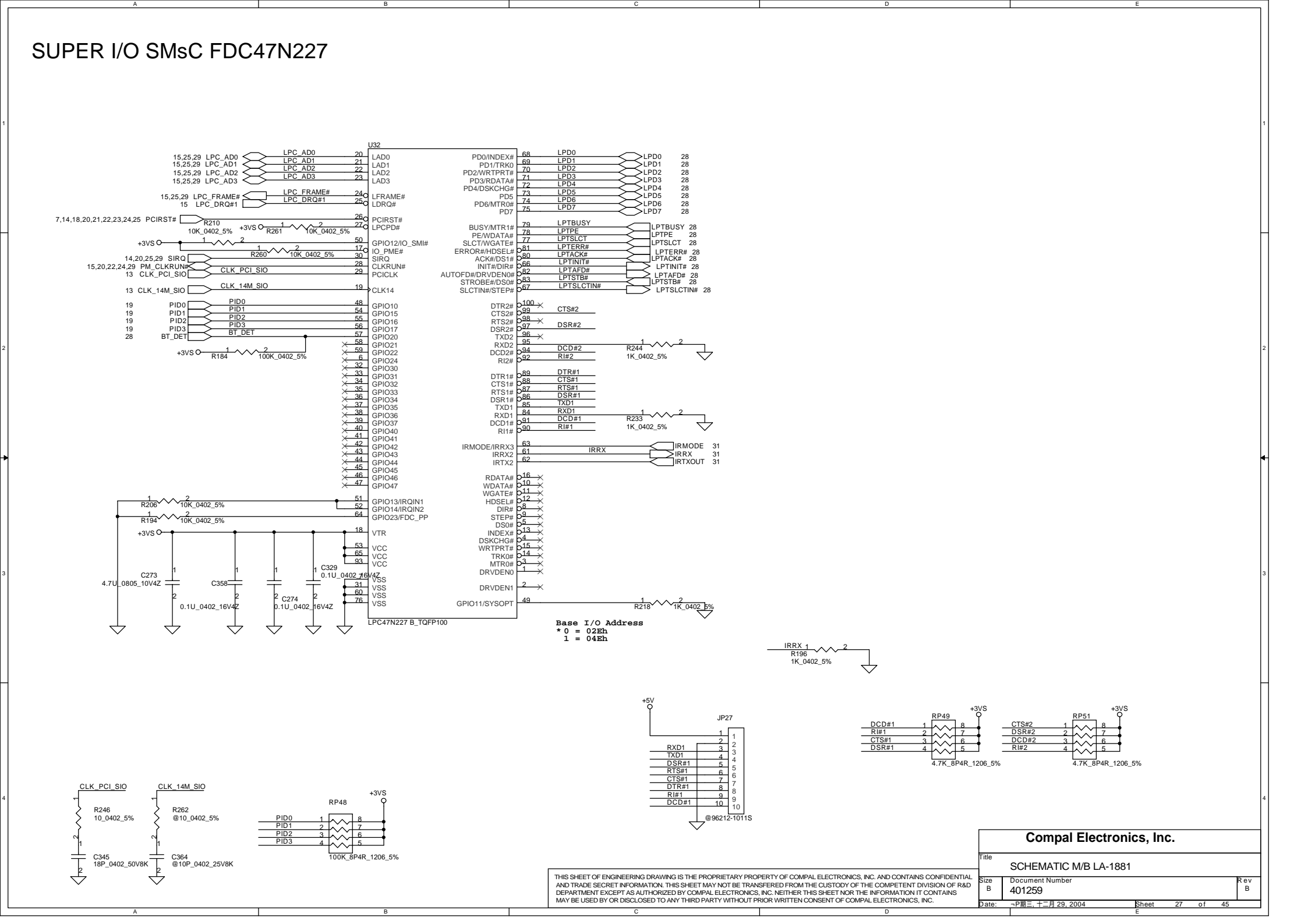
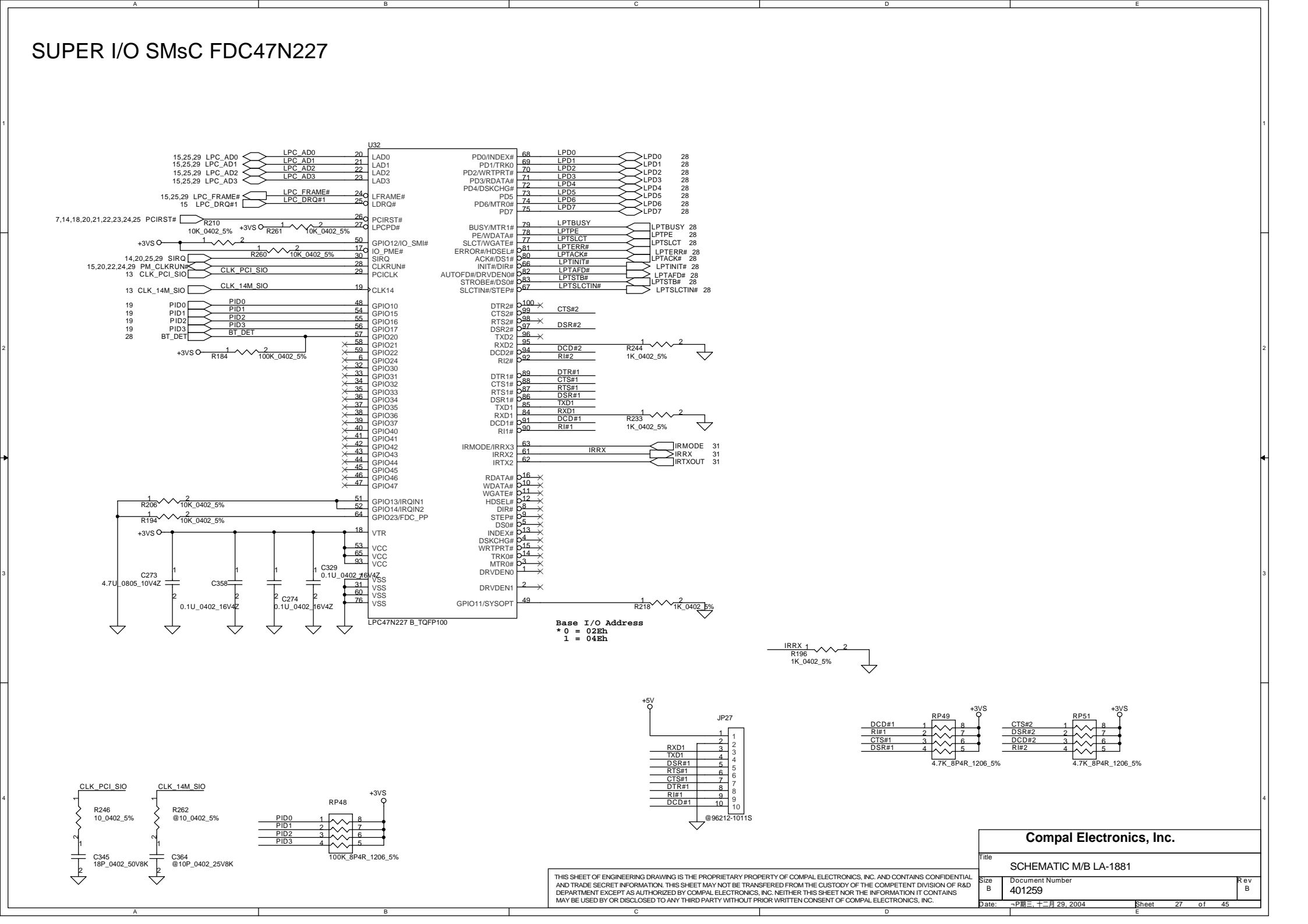
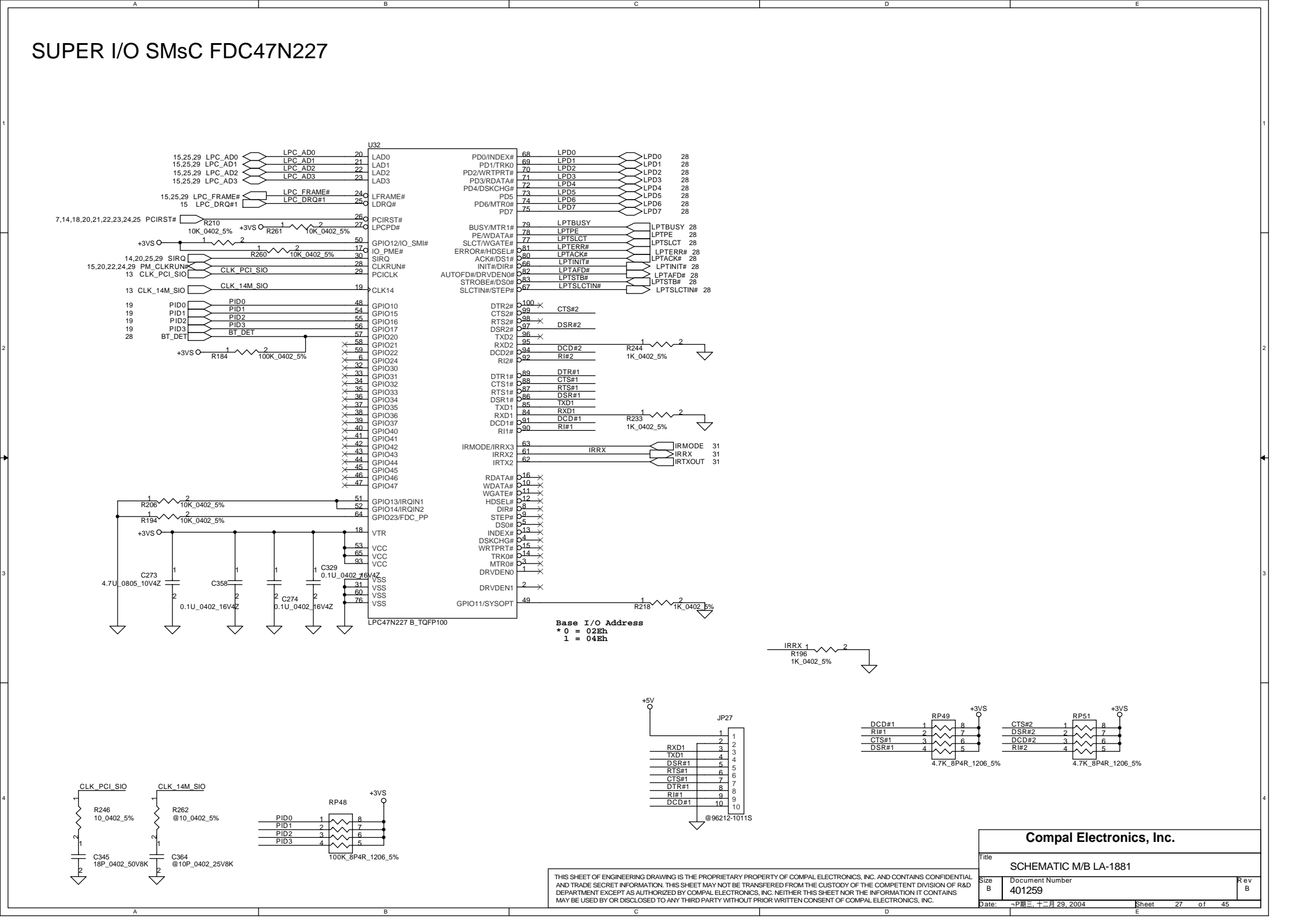
Reserve For ABO



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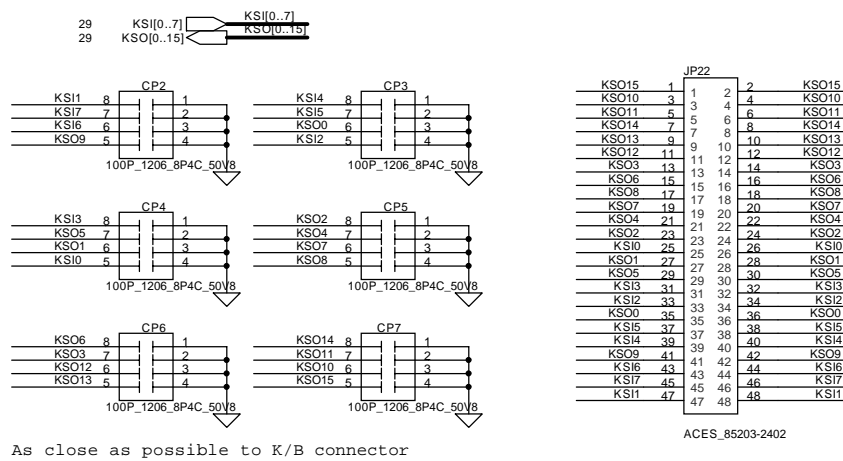
SUPER I/O SMsC FDC47N227

The schematic diagram illustrates the internal architecture and external connections of the SUPER I/O SMsC FDC47N227. The central component is the U32 chip, which is connected to various external components and pins.

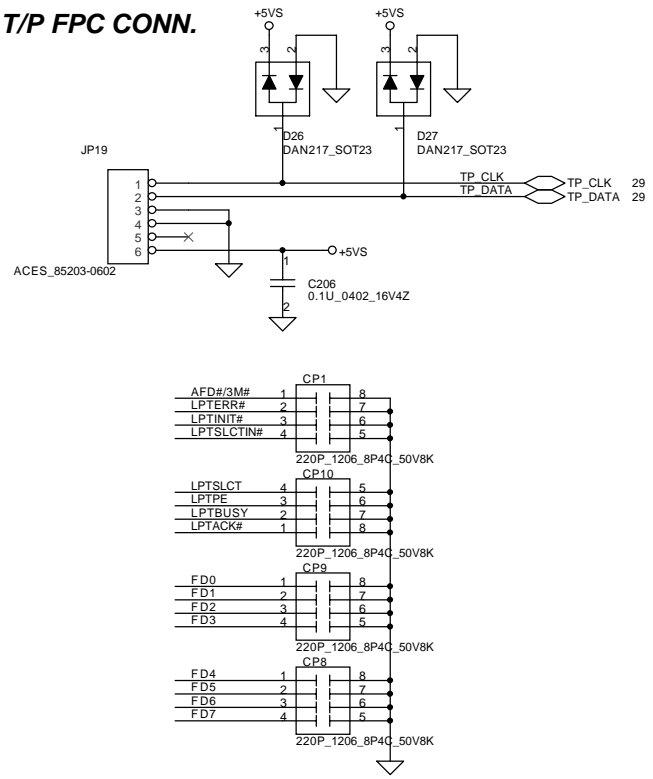
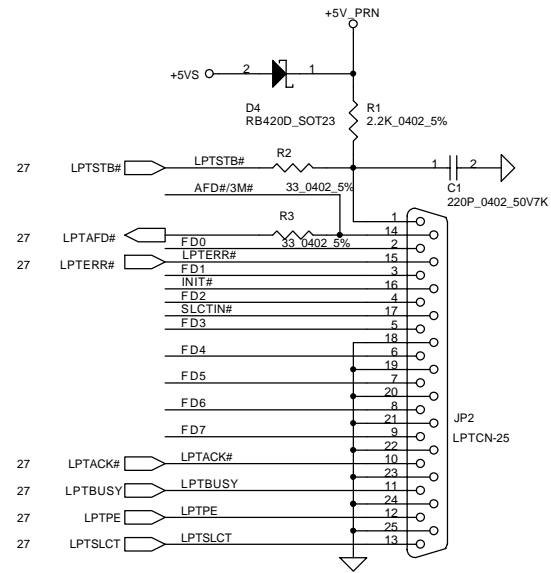
Internal Components and Connections:

- Resistors:** R210, R261, R260, R184, R206, R194, R246, R262, R245, R244, R233, R218, R196, R249, R251, R248, R247, R246, R245, R244, R243, R242, R241, R240, R239, R238, R237, R236, R235, R234, R233, R232, R231, R230, R229, R228, R227, R226, R225, R224, R223, R222, R221, R220, R219, R218, R217, R216, R215, R214, R213, R212, R211, R210, R209, R208, R207, R206, R205, R204, R203, R202, R201, R200, R199, R198, R197, R196, R195, R194, R193, R192, R191, R190, R189, R188, R187, R186, R185, R184, R183, R182, R181, R180, R179, R178, R177, R176, R175, R174, R173, R172, R171, R170, R169, R168, R167, R166, R165, R164, R163, R162, R161, R160, R159, R158, R157, R156, R155, R154, R153, R152, R151, R150, R149, R148, R147, R146, R145, R144, R143, R142, R141, R140, R139, R138, R137, R136, R135, R134, R133, R132, R131, R130, R129, R128, R127, R126, R125, R124, R123, R122, R121, R120, R119, R118, R117, R116, R115, R114, R113, R112, R111, R110, R109, R108, R107, R106, R105, R104, R103, R102, R101, R100, R99, R98, R97, R96, R95, R94, R93, R92, R91, R90, R89, R88, R87, R86, R85, R84, R83, R82, R81, R80, R79, R78, R77, R76, R75, R74, R73, R72, R71, R70, R69, R68, R67, R66, R65, R64, R63, R62, R61, R60, R59, R58, R57, R56, R55, R54, R53, R52, R51, R50, R49, R48, R47, R46, R45, R44, R43, R42, R41, R40, R39, R38, R37, R36, R35, R34, R33, R32, R31, R30, R29, R28, R27, R26, R25, R24, R23, R22, R21, R20, R19, R18, R17, R16, R15, R14, R13, R12, R11, R10, R9, R8, R7, R6, R5, R4, R3, R2, R1, R0.
- Capacitors:** C273, C358, C274, C329, C345, C364, C346, C347, C348, C349, C350, C351, C352, C353, C354, C355, C356, C357, C358, C359, C360, C361, C362, C363, C364, C365, C366, C367, C368, C369, C370, C371, C372, C373, C374, C375, C376, C377, C378, C379, C380, C381, C382, C383, C384, C385, C386, C387, C388, C389, C390, C391, C392, C393, C394, C395, C396, C397, C398, C399, C400, C401, C402, C403, C404, C405, C406, C407, C408, C409, C410, C411, C412, C413, C414, C415, C416, C417, C418, C419, C420, C421, C422, C423, C424, C425, C426, C427, C428, C429, C430, C431, C432, C433, C434, C435, C436, C437, C438, C439, C440, C441, C442, C443, C444, C445, C446, C447, C448, C449, C450, C451, C452, C453, C454, C455, C456, C457, C458, C459, C460, C461, C462, C463, C464, C465, C466, C467, C468, C469, C470, C471, C472, C473, C474, C475, C476, C477, C478, C479, C480, C481, C482, C483, C484, C485, C486, C487, C488, C489, C490, C491, C492, C493, C494, C495, C496, C497, C498, C499, C500, C501, C502, C503, C504, C505, C506, C507, C508, C509, C510, C511, C512, C513, C514, C515, C516, C517, C518, C519, C520, C521, C522, C523, C524, C525, C526, C527, C528, C529, C530, C531, C532, C533, C534, C535, C536, C537, C538, C539, C540, C541, C542, C543, C544, C545, C546, C547, C548, C549, C550, C551, C552, C553, C554, C555, C556, C557, C558, C559, C560, C561, C562, C563, C564, C565, C566, C567, C568, C569, C570, C571, C572, C573, C574, C575, C576, C577, C578, C579, C580, C581, C582, C583, C584, C585, C586, C587, C588, C589, C590, C591, C592, C593, C594, C595, C596, C597, C598, C599, C600, C601, C602, C603, C604, C605, C606, C607, C608, C609, C610, C611, C612, C613, C614, C615, C616, C617, C618, C619, C620, C621, C622, C623, C624, C625, C626, C627, C628, C629, C630, C631, C632, C633, C634, C635, C636, C637, C638, C639, C640, C641, C642, C643, C644, C645, C646, C647, C648, C649, C650, C651, C652, C653, C654, C655, C656, C657, C658, C659, C660, C661, C662, C663, C664, C665, C666, C667, C668, C669, C670, C671, C672, C673, C674, C675, C676, C677, C678, C679, C680, C681, C682, C683, C684, C685, C686, C687, C688, C689, C690, C691, C692, C693, C694, C695, C696, C697, C698, C699, C700, C701, C702, C703, C704, C705, C706, C707, C708, C709, C710, C711, C712, C713, C714, C715, C716, C717, C718, C719, C720, C721, C722, C723, C724, C725, C726, C727, C728, C729, C730, C731, C732, C733, C734, C735, C736, C737, C738, C739, C740, C741, C742, C743, C744, C745, C746, C747, C748, C749, C750, C751, C752, C753, C754, C755, C756, C757, C758, C759, C760, C761, C762, C763, C764, C765, C766, C767, C768, C769, C770, C771, C772, C773, C774, C775, C776, C777, C778, C779, C780, C781, C782, C783, C784, C785, C786, C787, C788, C789, C790, C791, C792, C793, C794, C795, C796, C797, C798, C799, C800, C801, C802, C803, C804, C805, C806, C807, C808, C809, C810, C811, C812, C813, C814, C815, C816, C817, C818, C819, C820, C821, C822, C823, C824, C825, C826, C827, C828, C829, C830, C831, C832, C833, C834, C835, C836, C837, C838, C839, C840, C841, C842, C843, C844, C845, C846, C847, C848, C849, C850, C851, C852, C853, C854, C855, C856, C857, C858, C859, C860, C861, C862, C863, C864, C865, C866, C867, C868, C869, C870, C871, C872, C873, C874, C875, C876, C877, C878, C879, C880, C881, C882, C883, C884, C885, C88

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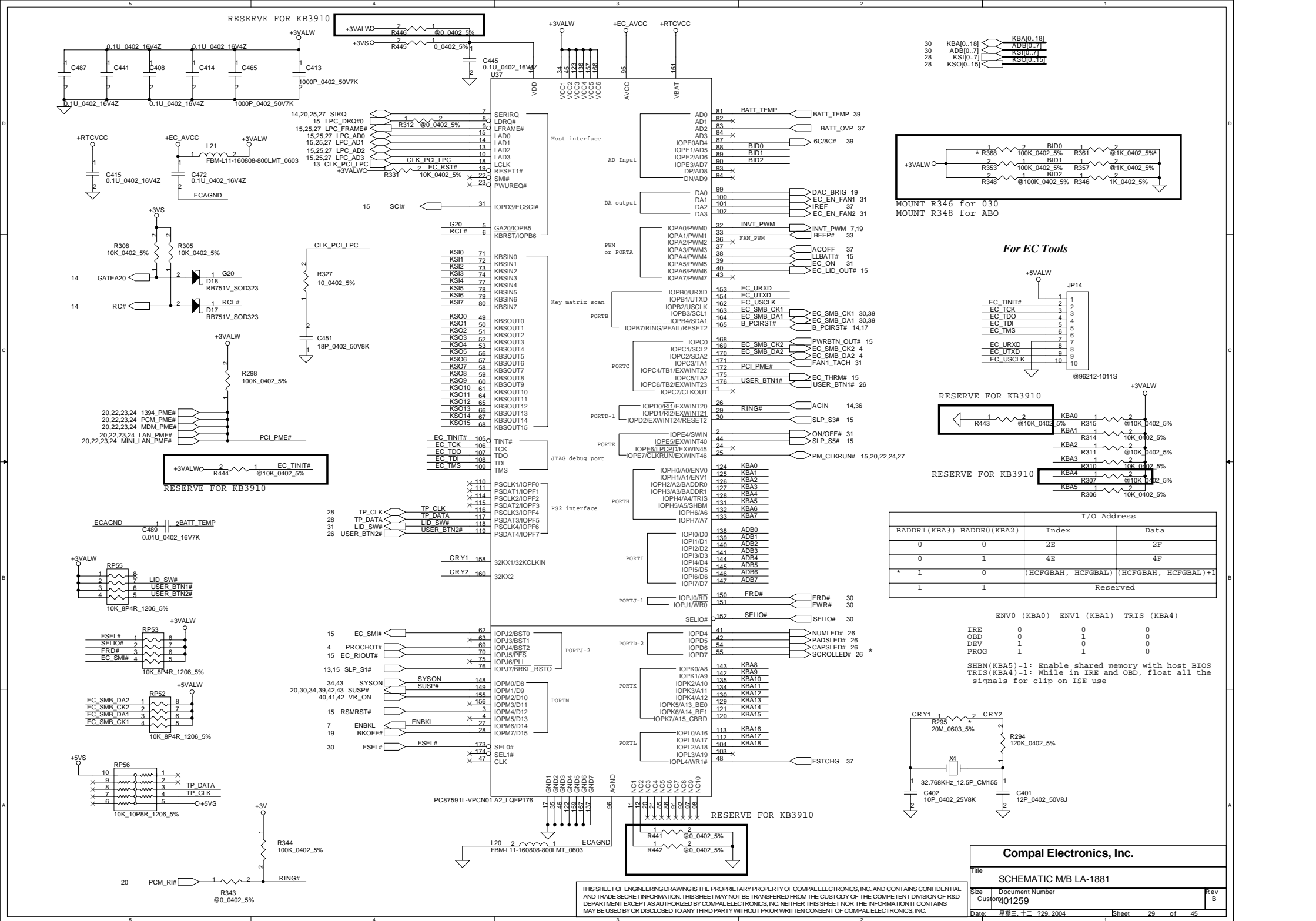
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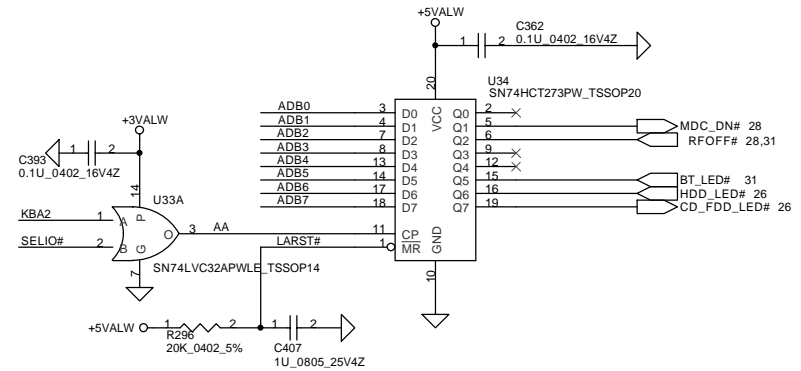
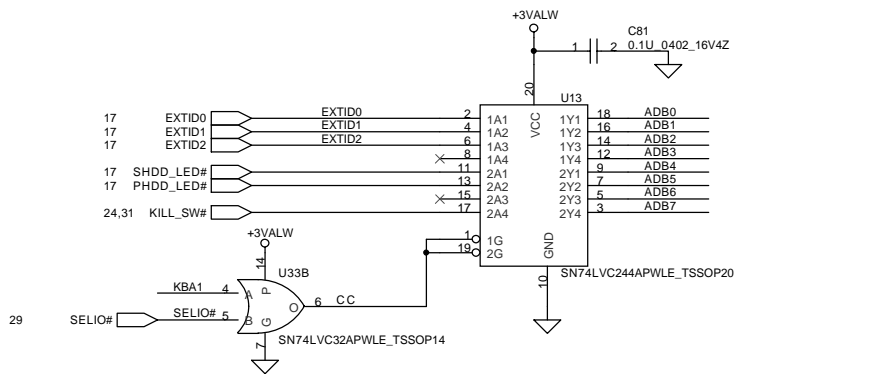


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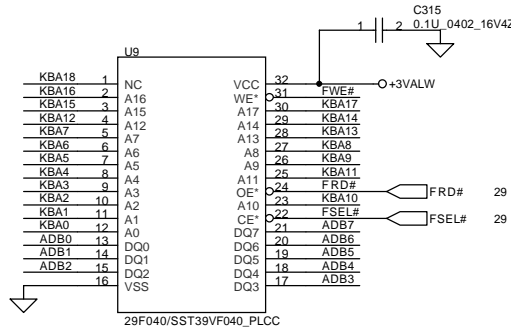
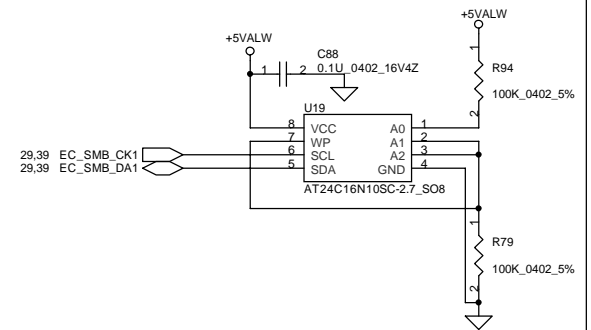
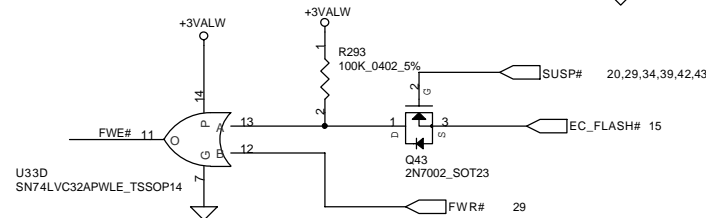
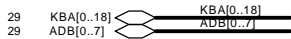
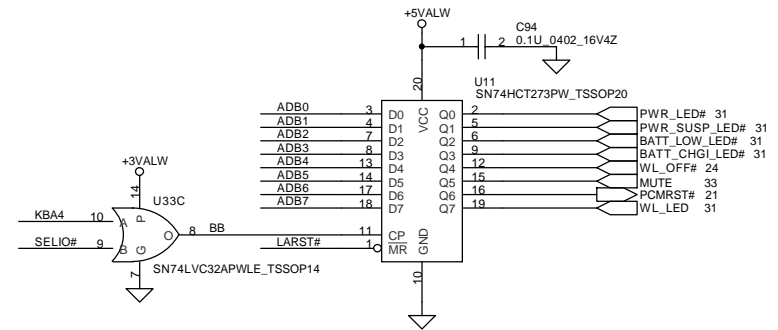
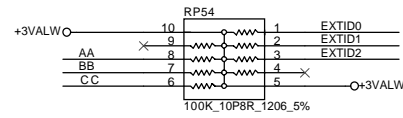
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EXTID2	EXTID1	EXTID0	Module
0	0	0	FDD
0	0	1	CDROM
0	1	1	2ndHDD
1	1	1	NO Module



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Power ON Circuit

29 LID_SW#

LID_SW#

D14
@DAN217_SOT23

SW1

LID_SWITCH

+3VALW

+3V

R400
180K_0402_5%

C1

U43D
SN74LVC125APWLE_TSSOP14

U40F

U40E

U44B
@LM358A_SOB

[illegible][illegible][illegible]

Suspend LED(Amber)

Power LED(Blue)

30 PWR_SUSP_LED#

Q12
DTA114YKA_SOT23

26 PWR_SUSP_LED

30 WL_LED

30 BATT_LOW_LED#

Q13
DTA114YKA_SOT23

26 BATT_LOW_LED

30 BATT_CHGI_LED#

26 BATT_CHGI_LED

Suspend LED(Amber)

Power LED(Blue)

30 PWR_SUSP_LED#

Q12
DTA114YKA_SOT23

26 PWR_SUSP_LED

30 WL_LED

30 BATT_LOW_LED#

Q13
DTA114YKA_SOT23

26 BATT_LOW_LED

30 BATT_CHGI_LED#

26 BATT_CHGI_LED

Wireless LED(Amber)

Q2 2N7002_SOT23

R9 100K_0402_5%

Q8 DTA114YKA_SOT23

R26 300_0603_5%

WLED

Bluetooth LED(Blue)

BT_LED#

* Q50 DTA114YKA_SOT23

R454 * 240_0603_5%

BTLED

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Wireless LED(Amber)

Q2 2N7002_SOT23

R9 100K_0402_5%

Q8 DTA114YKA_SOT23

R26 300_0603_5%

WLED

Bluetooth LED(Blue)

BT_LED#

* Q50 DTA114YKA_SOT23

R454 * 240_0603_5%

BTLED

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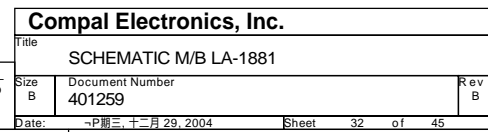
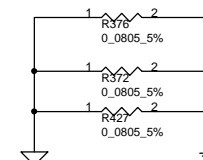
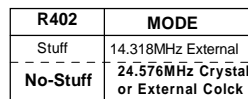
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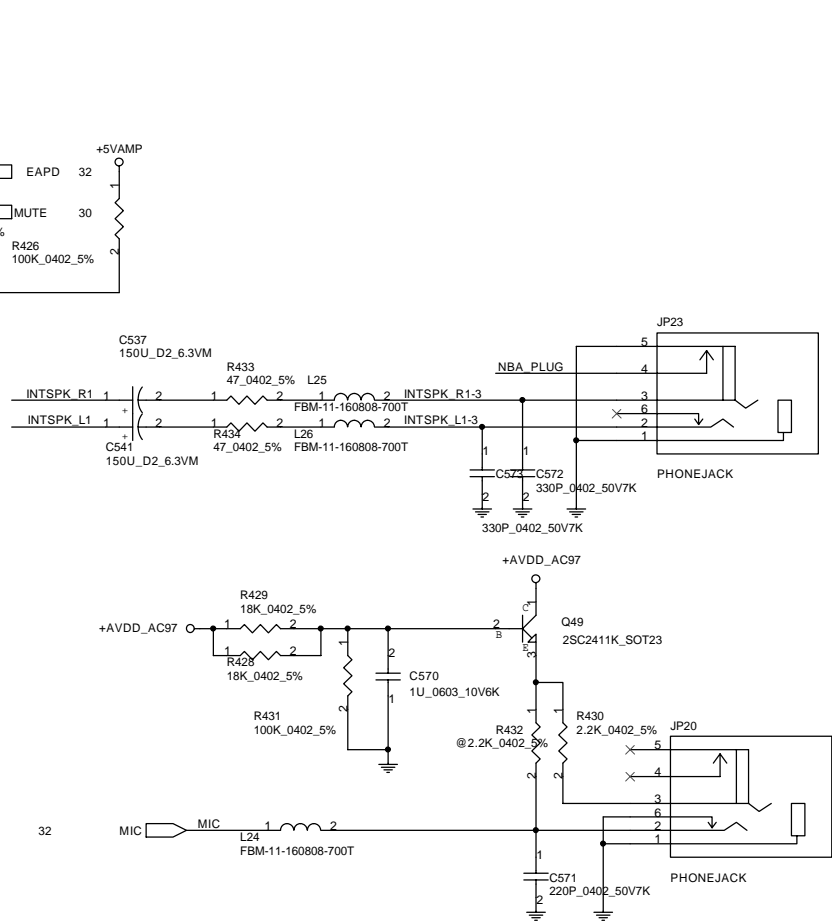
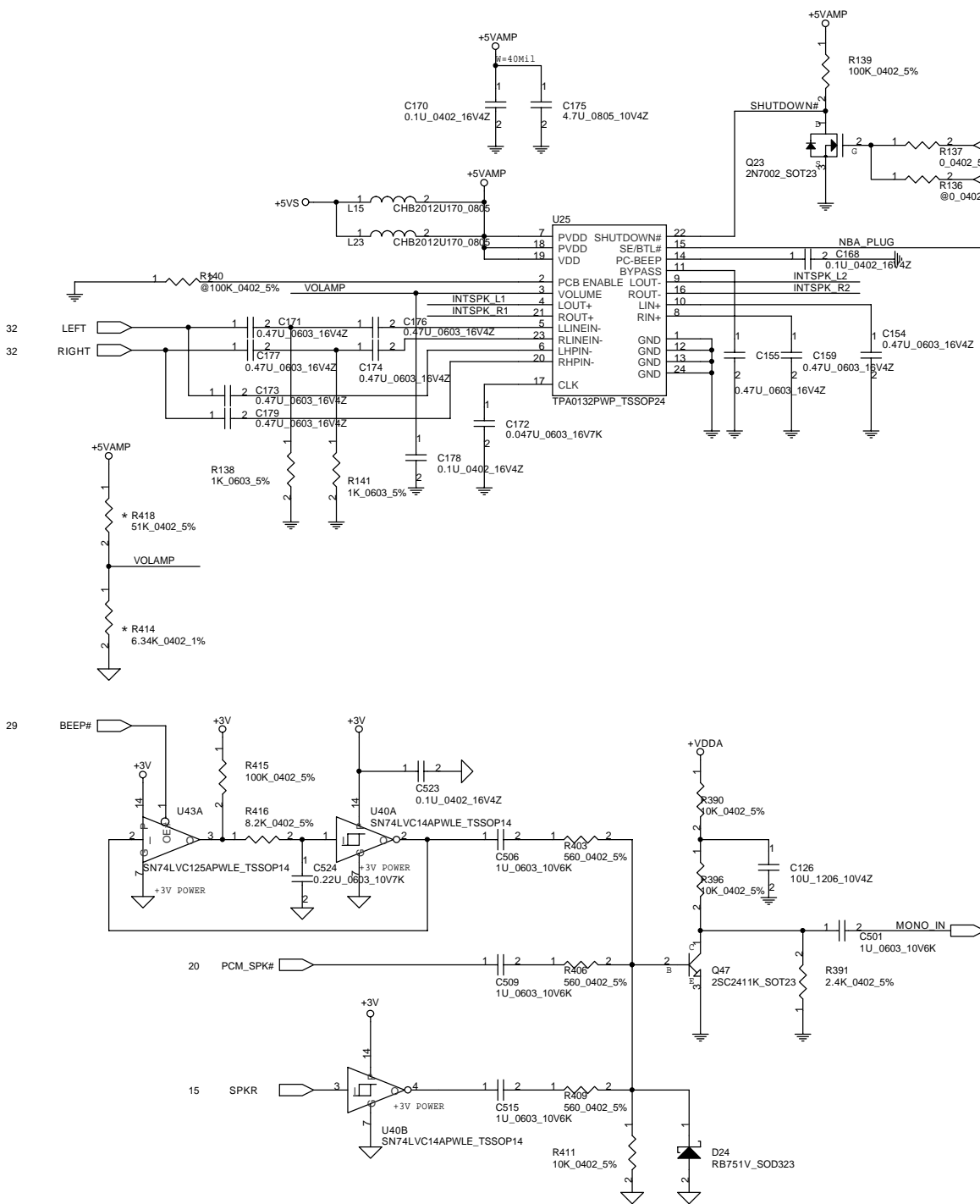
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Size	Document Number 401259		Rev B
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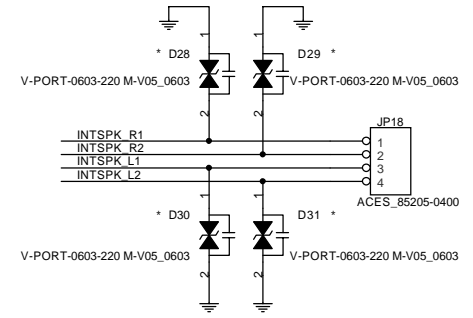
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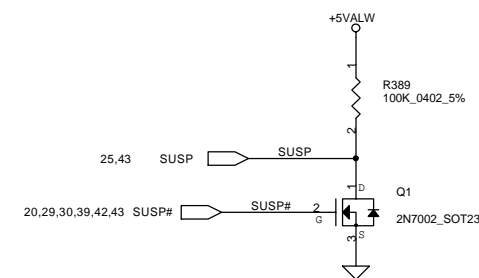
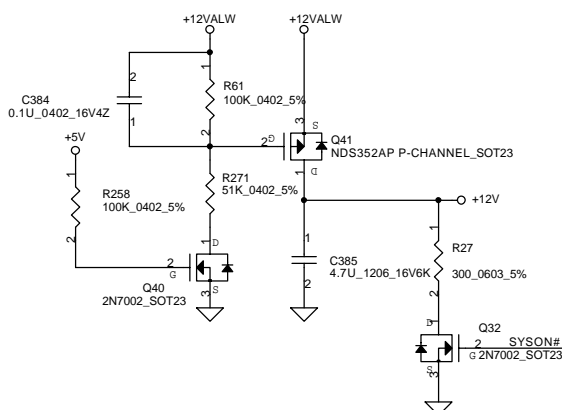
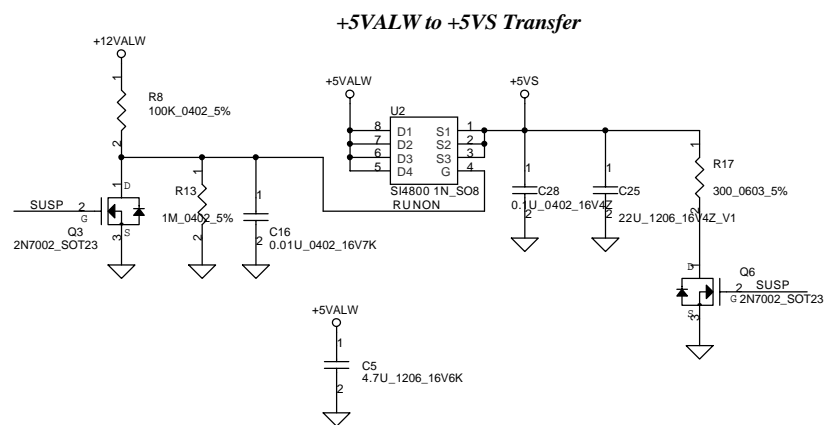
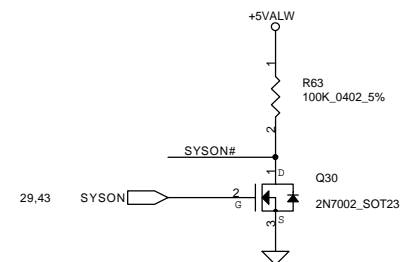
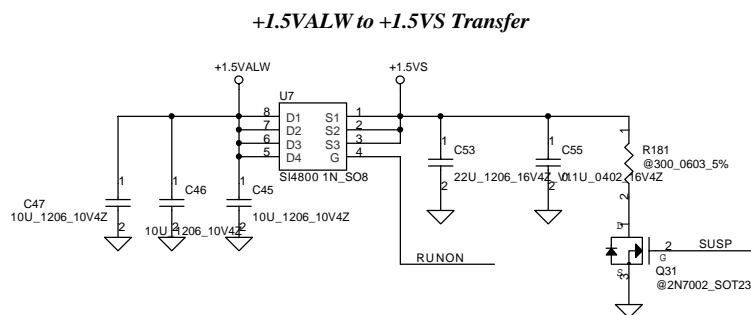
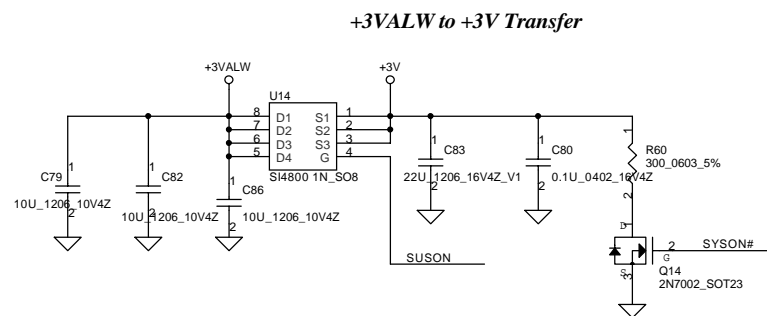
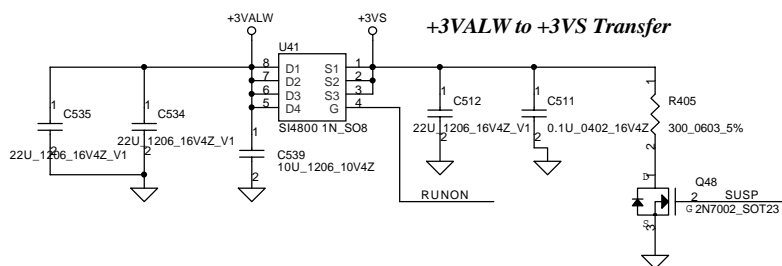
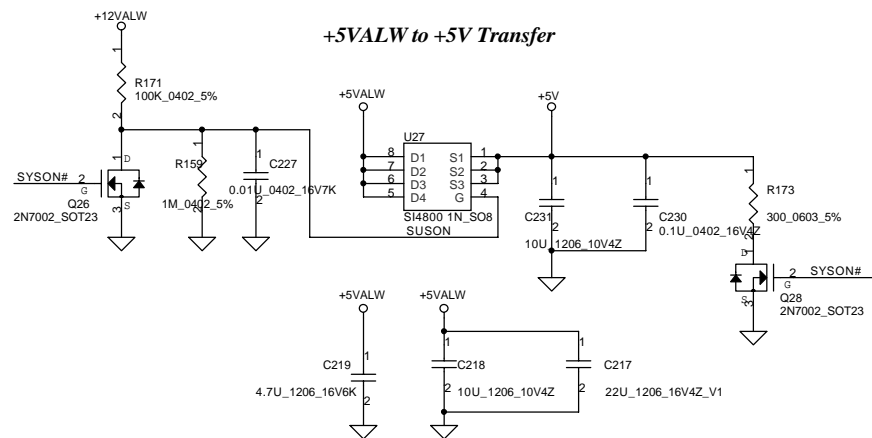


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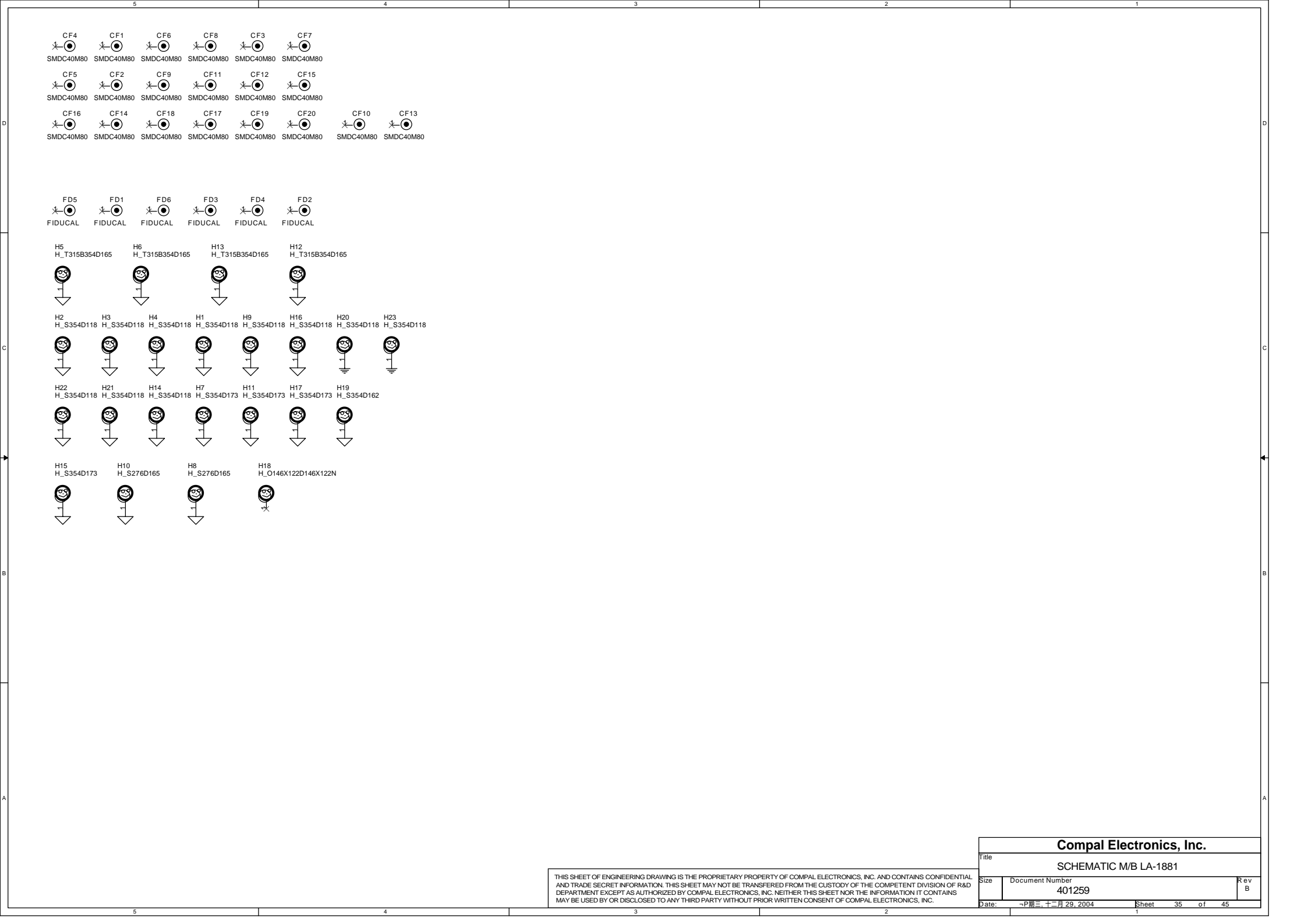
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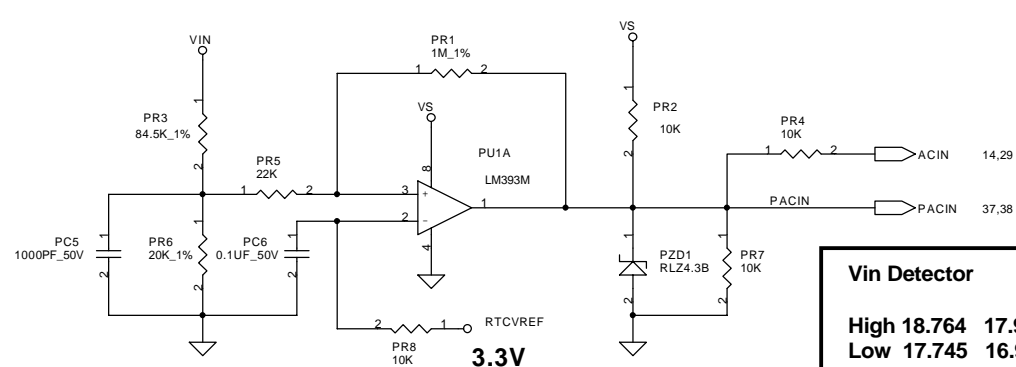
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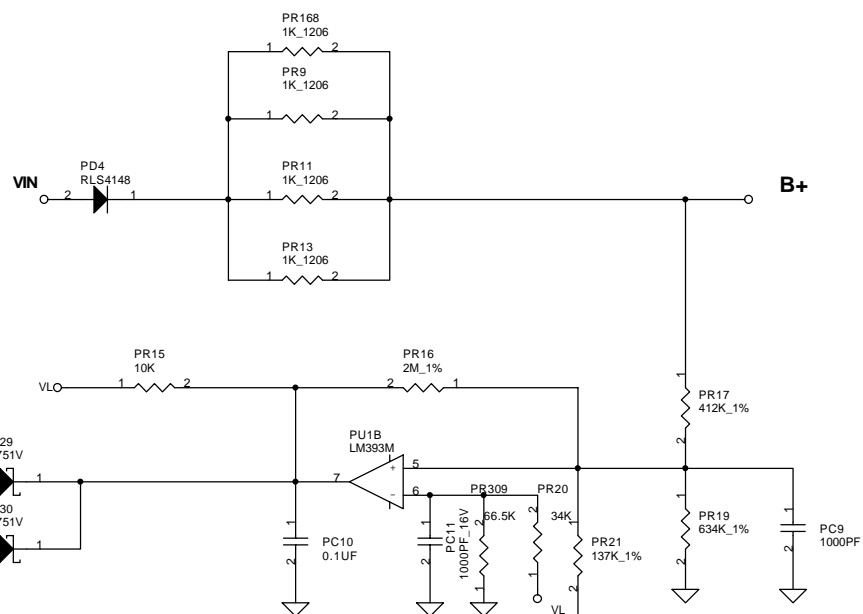


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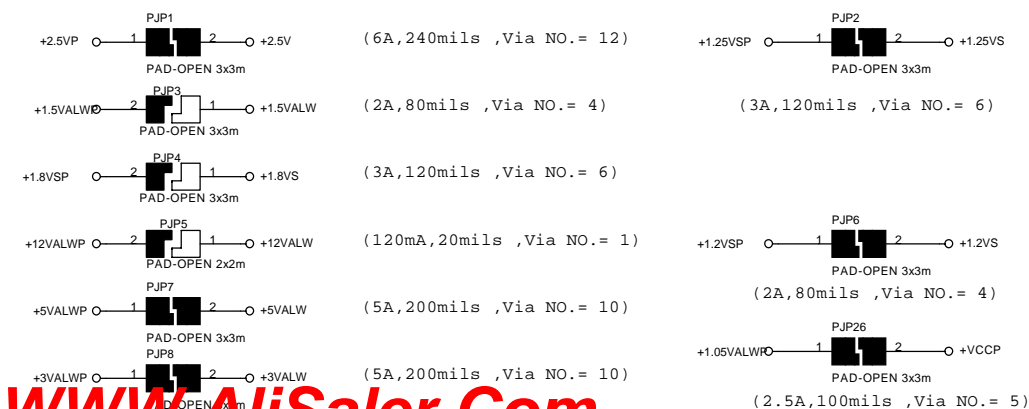


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Low	17.745	16.903	16.038



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15.6	15.02	14.35

6.33	6.12	5.92
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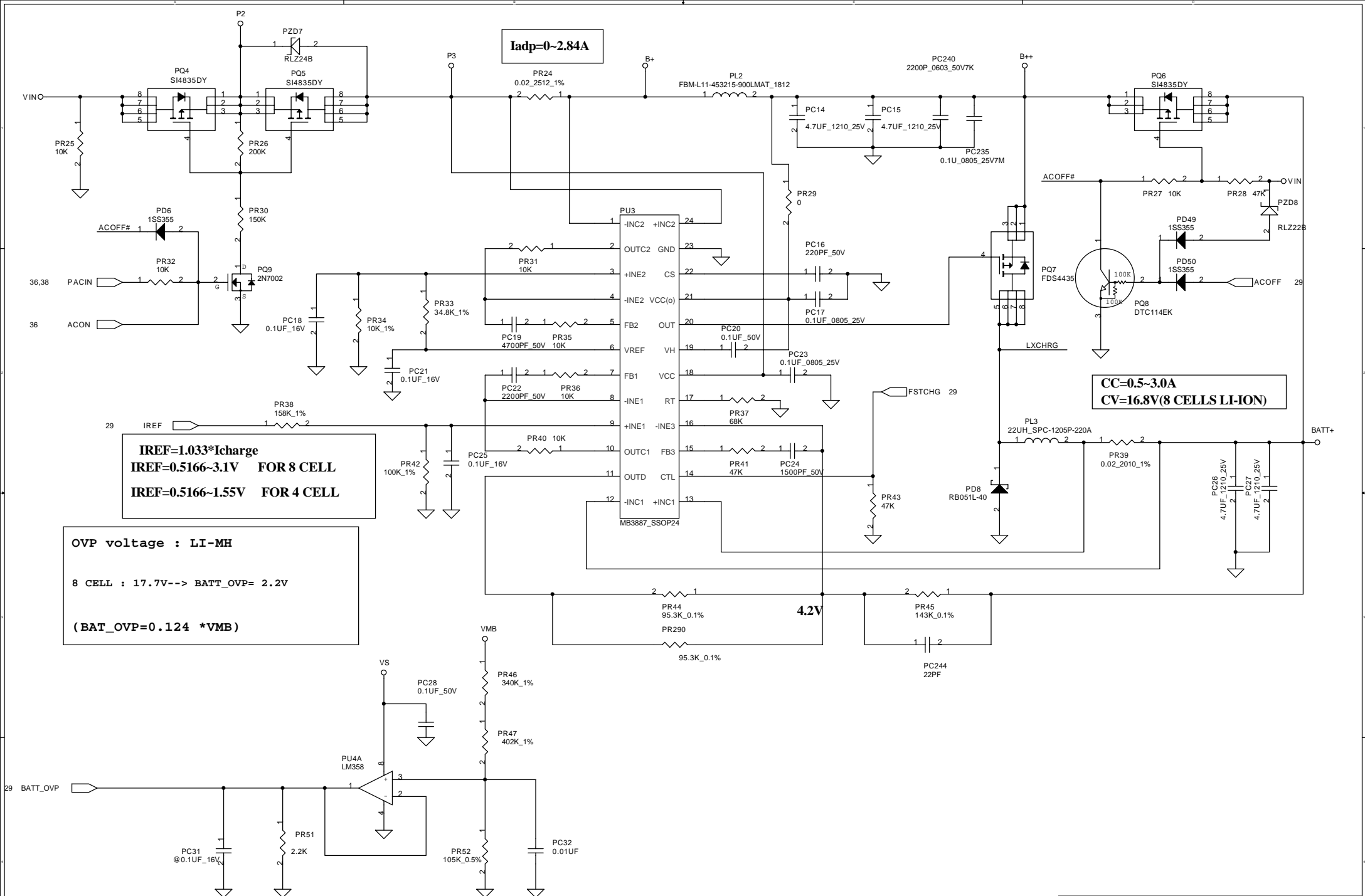


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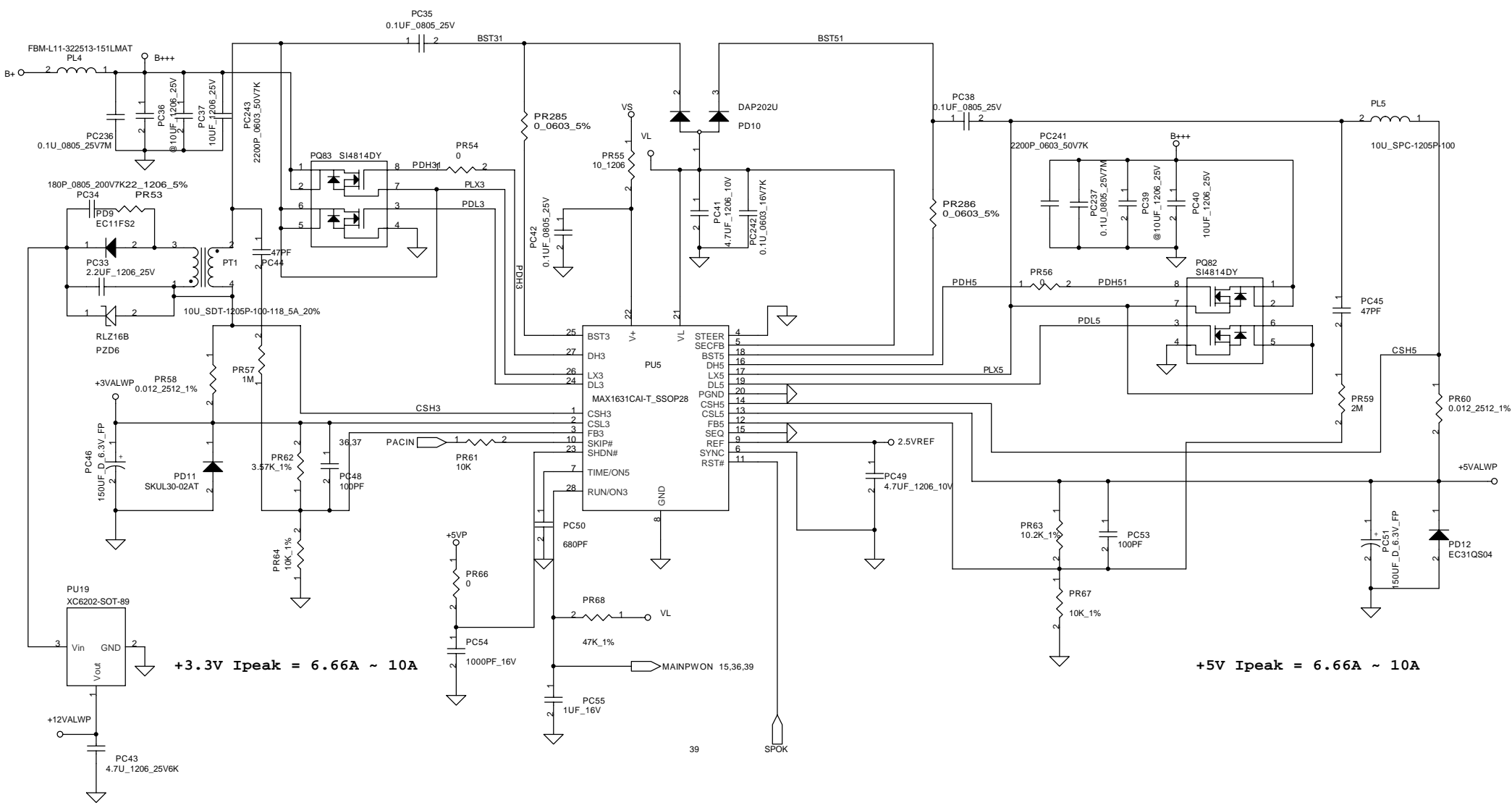
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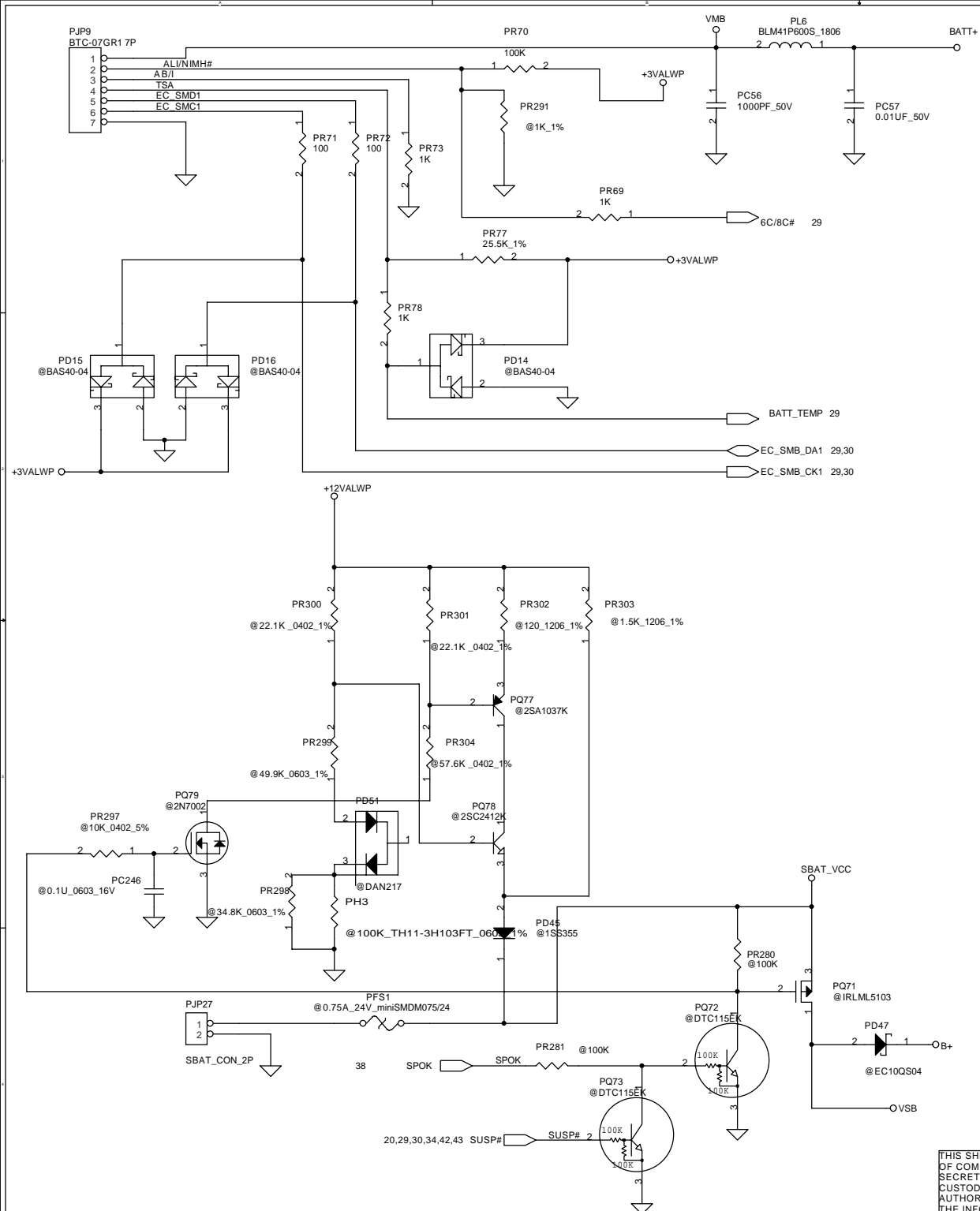
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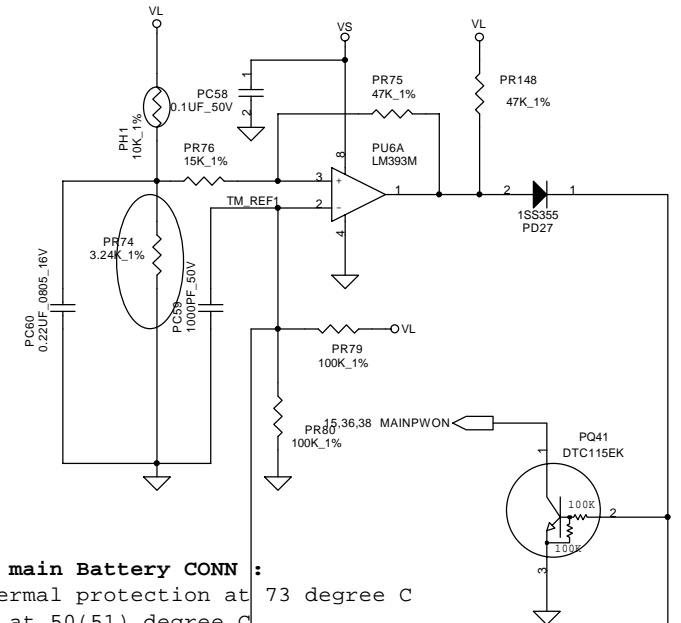
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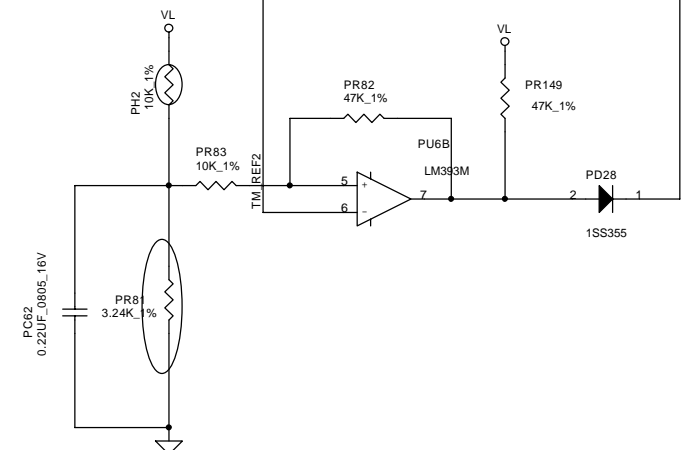
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PH1 under CPU botten side :
 CPU thermal protection at 82 degree C
 Recovery at 48 degree C



PH2 near main Battery CONN :
 BAT. thermal protection at 73 degree C
 Recovery at 50(51) degree C

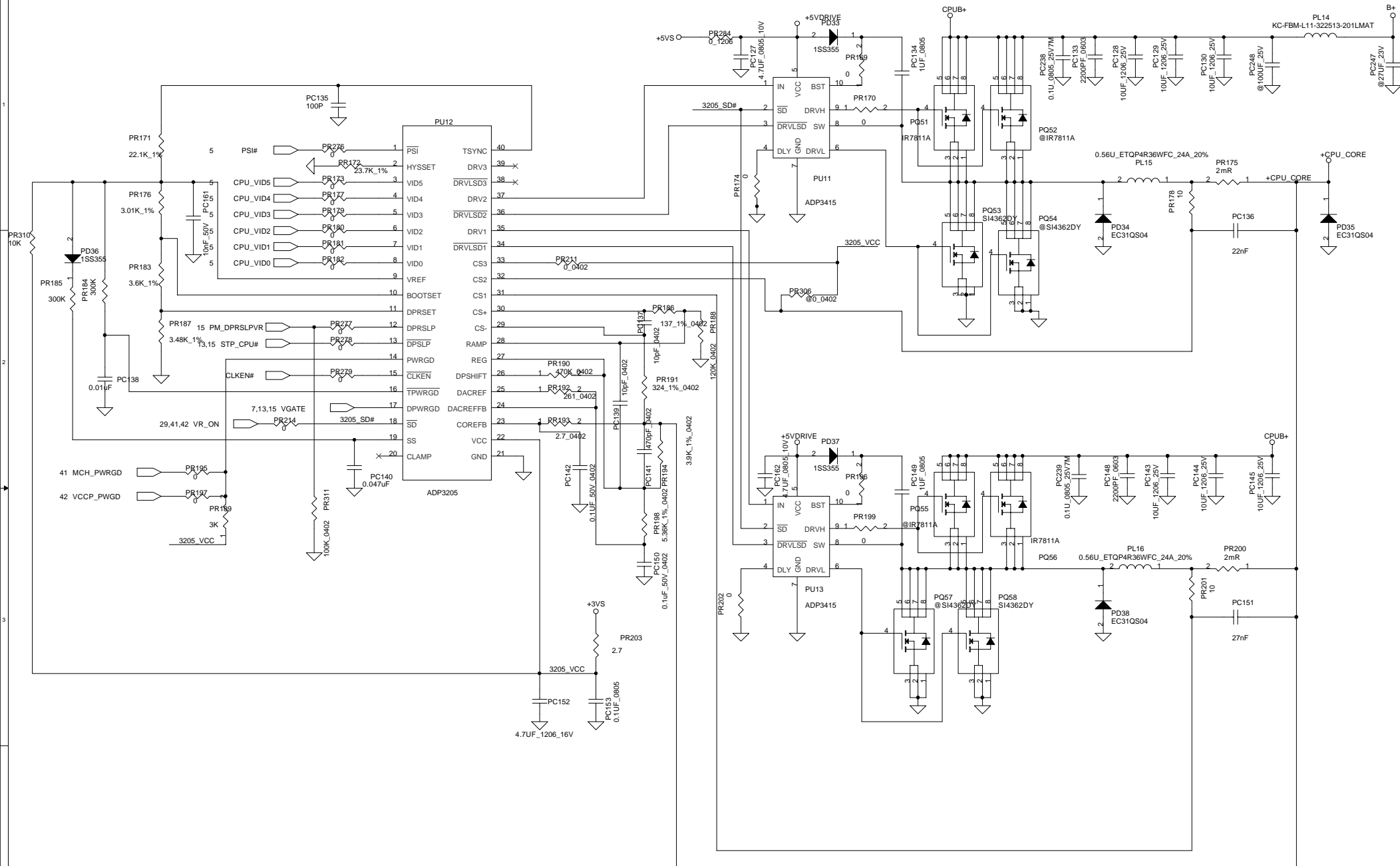


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CPU-CORE



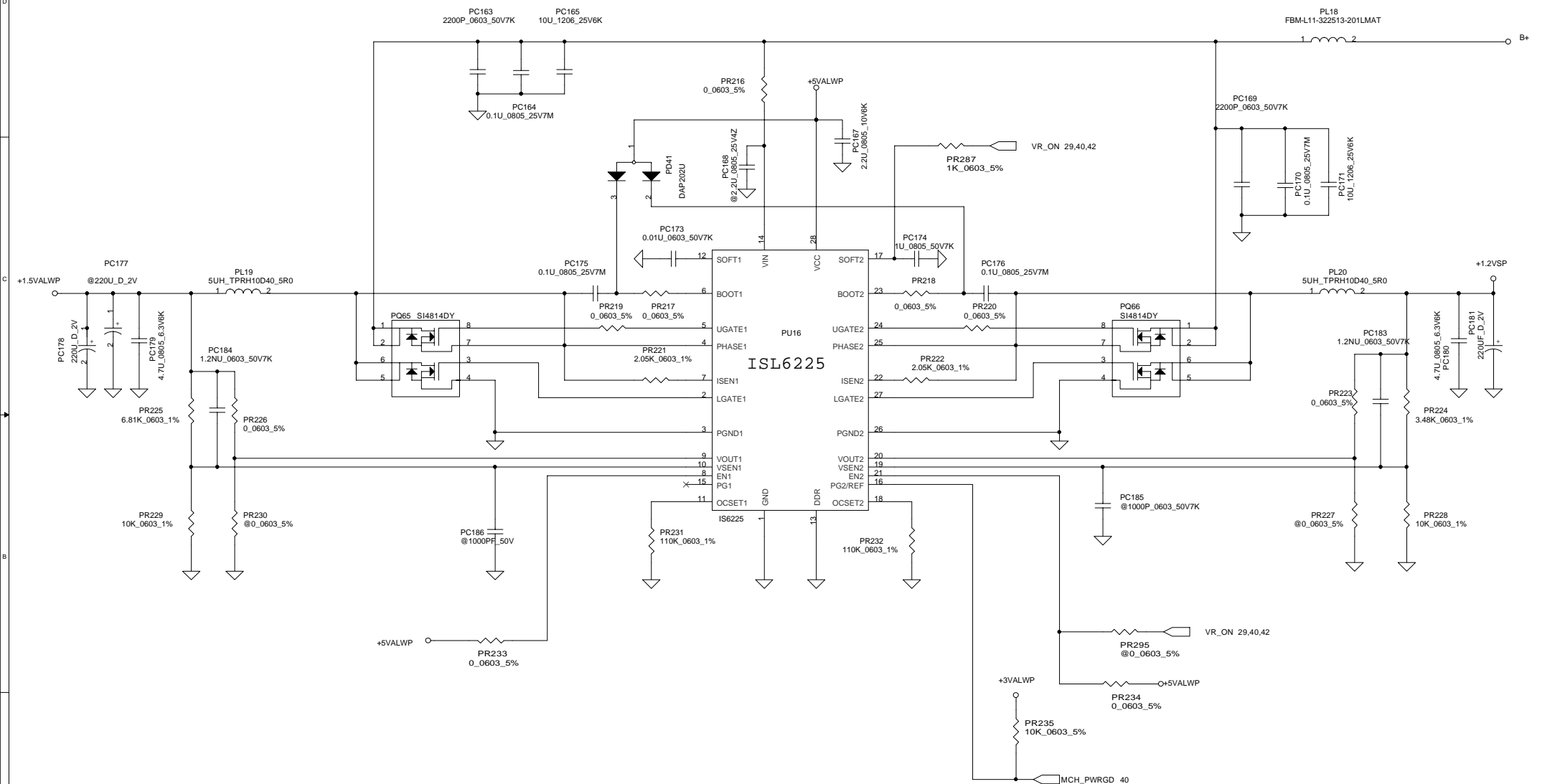
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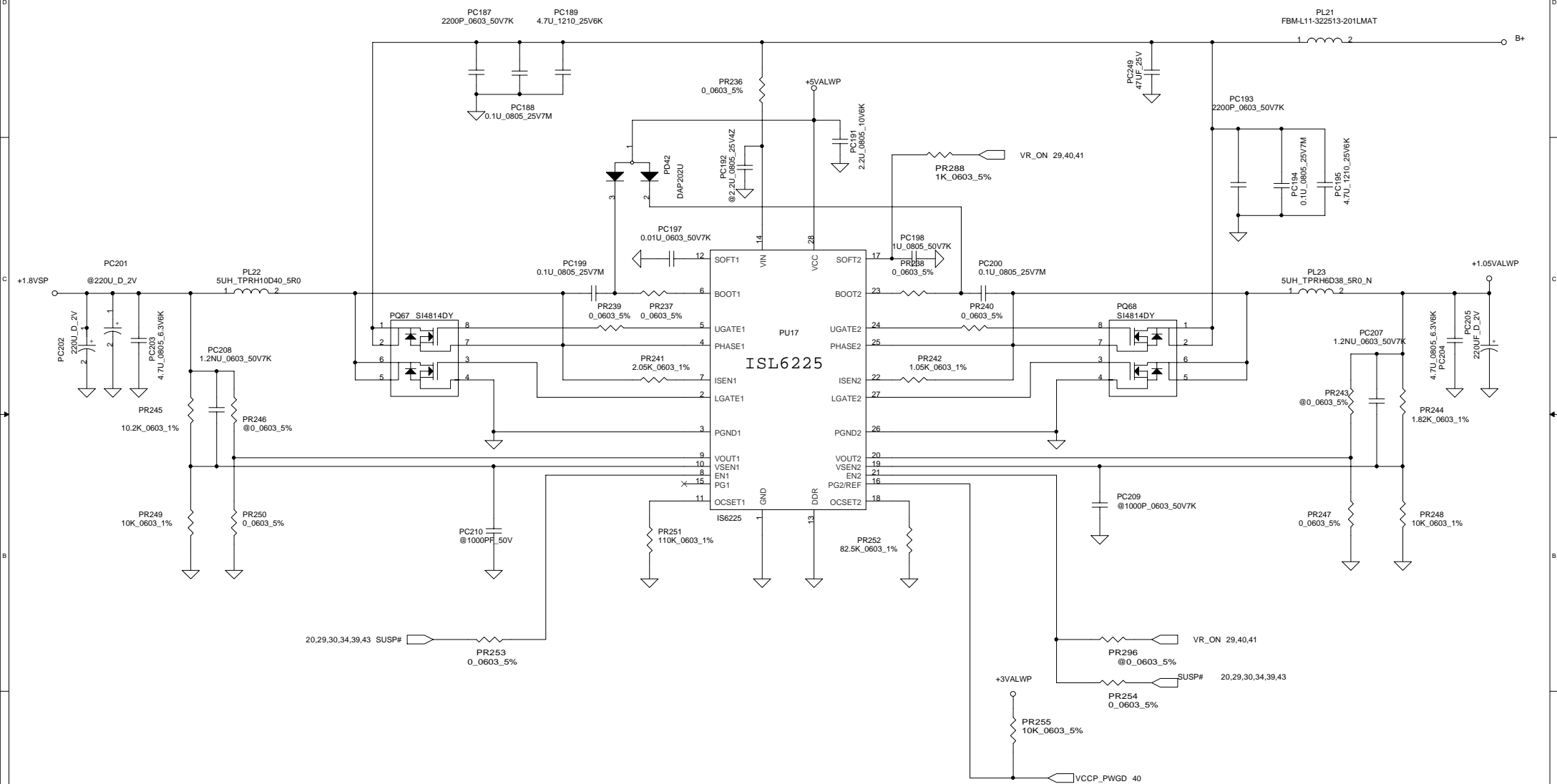
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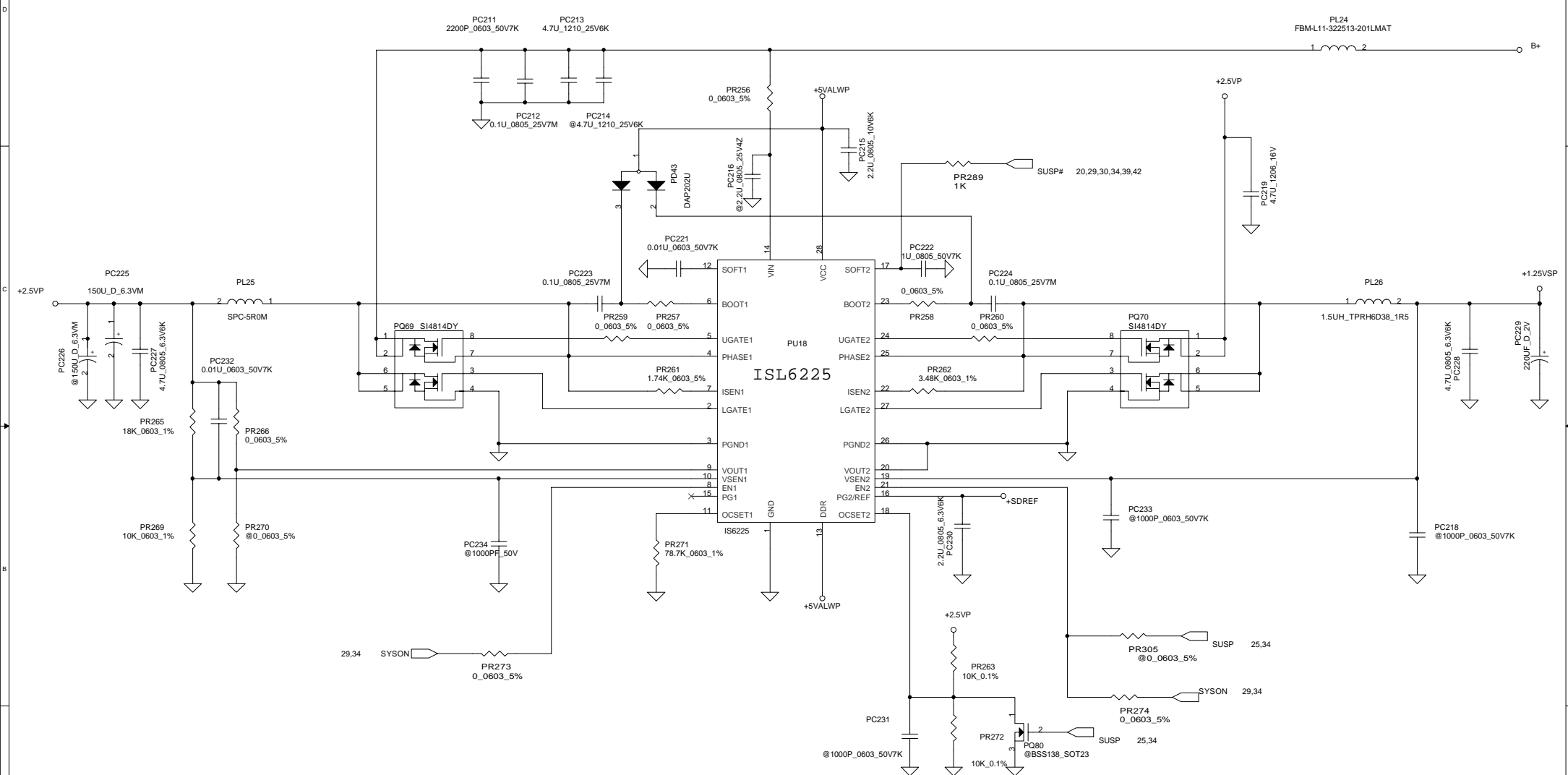
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+2.5VP/+1.25VSP



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Version change list (P.I.R. List)

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for HW

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1.	+5VMOD leaked when AC-IN	Change EXTIDEPWR# to +5VALW	0.2	P17	Delete EXTIDEPWR# Add +5VALW to R126		DVT
2.	For ESD	For ESD	0.2	P28	Add D26, D27 on TP_DATA, TP_CLK		DVT
3.	For EMI	For EMI	0.2	P31	Add C574 on FAN1_TACH		DVT
4.	Reserve For ENE KB3910	Reserve For ENE KB3910	0.2	P29	Add R441, R442, R443, R444, R445		DVT
5.	Reserve For CB1410 Rev.B0	Reserve For CB1410 Rev.B0	0.2	P20,21	Add C575, C576, R447, R448, R449		DVT
6.	Reserve FAN Control Circuit	Reserve FAN Control Circuit	0.2	P31	Add U44, R450, R451, R452, R453, C577		DVT
7.	Bluetooth LED	Add Bluetooth LED	0.3	P31	Add Q50, R454		PVT
8.	Change LED Wire Connector	Change LED Wire Connector	0.3	P26	Change JP24 Connector Type		PVT
9.	Reserve SCROLL LED Circuit	Add LED Control Circuit	0.3	P26	Add Q51, Q52, R455, R456		PVT
10.	For ESD	For ESD	0.3	P33	Add D28, D29, D30, D31 close to Speaker Connector		PVT

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Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		delete 6 cell charger voltage		37	1.delete PQ75,PQ74,PR294,PC245		DVT
2		Adjust charger charge voltage:		37	1. change PU3 from MB3878 to MB3887, PR38 from 118K to 174k,PR33 from 26.1k to 34k, PR44 from 120k_1% to 9.3k_0.1%,PR45 from 243k_1% to 143k_1%, PR290 from 243k_1% to 143k_0.1%		
3		modify 1.5UH CHOKE footprint to be the same as all vender		42	Change PL26 fom old version:1.5UH TPRH6D38 to new version 1.5UH_TPRH6D38-N.		DVT
4		add pull low resistor PR311 to ground for hardware		39	add PR311		DVT
5		for cpu core current balance		40	change pc151 from 22n to 27n		PVT
6		for vender require , new DC JACKbe crease		36	increase PJP11		
7		for 1.2vs soft start rise time		41,42,43	change pc174,pc198,pc222, from 4.7u to 1u		
8		cost down		38,39,40,41	delete PC36,PC39,PC247,PC249 CHANGE PC195,PC189 FROM 10U 1206 TO 4.7U 1210		
9		TP0610C will be phase out		36	change TP0610C to TP0610K		

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